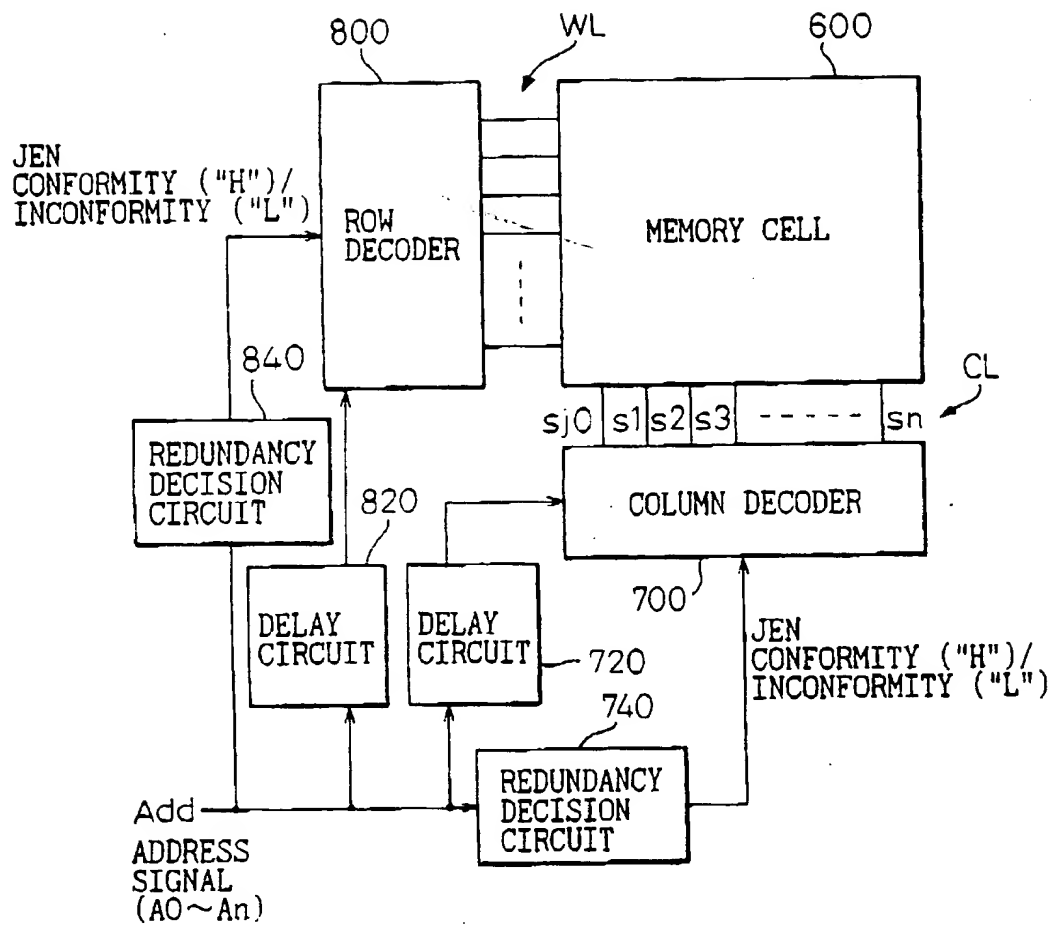


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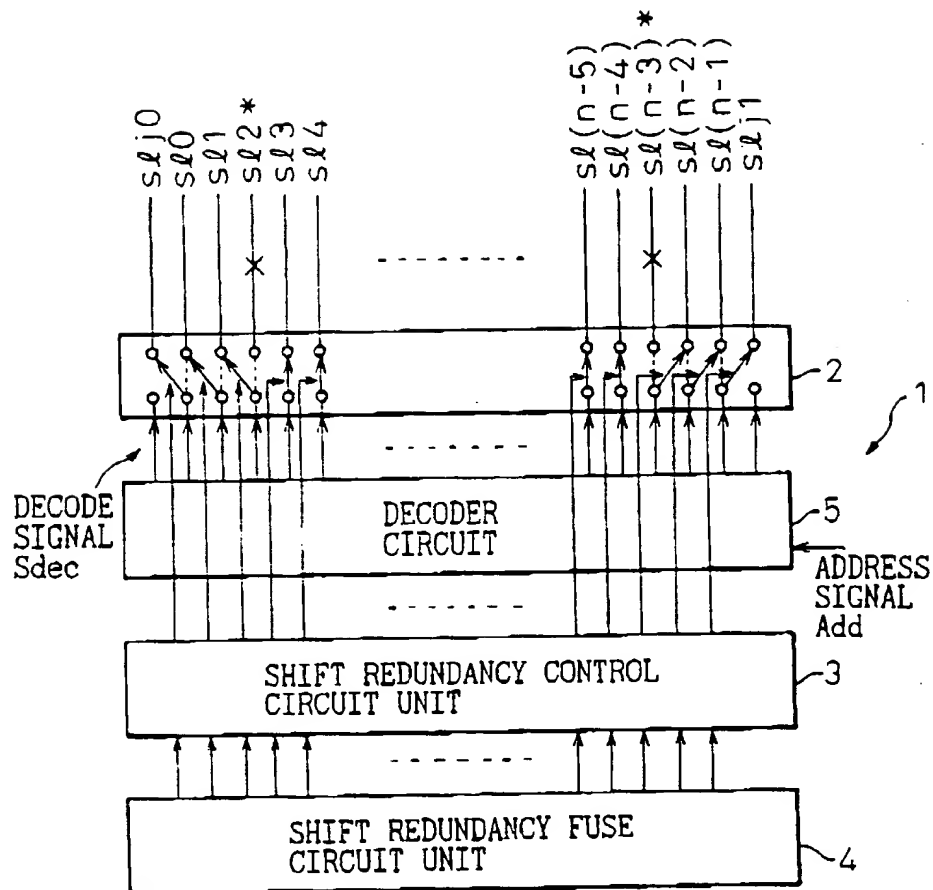
Fig.1



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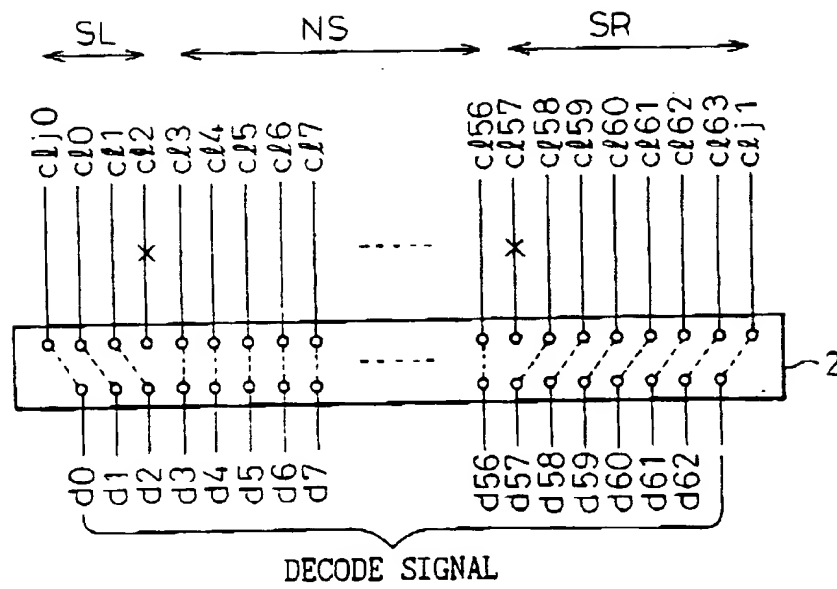
Fig.2



REMARKS)  
 FOR EXAMPLE, IT IS ASSUMED THAT  
 FAULT HAS OCCURRED IN EACH OF  
 SELECTING LINES  $sl2$  AND  $sl(n-3)$

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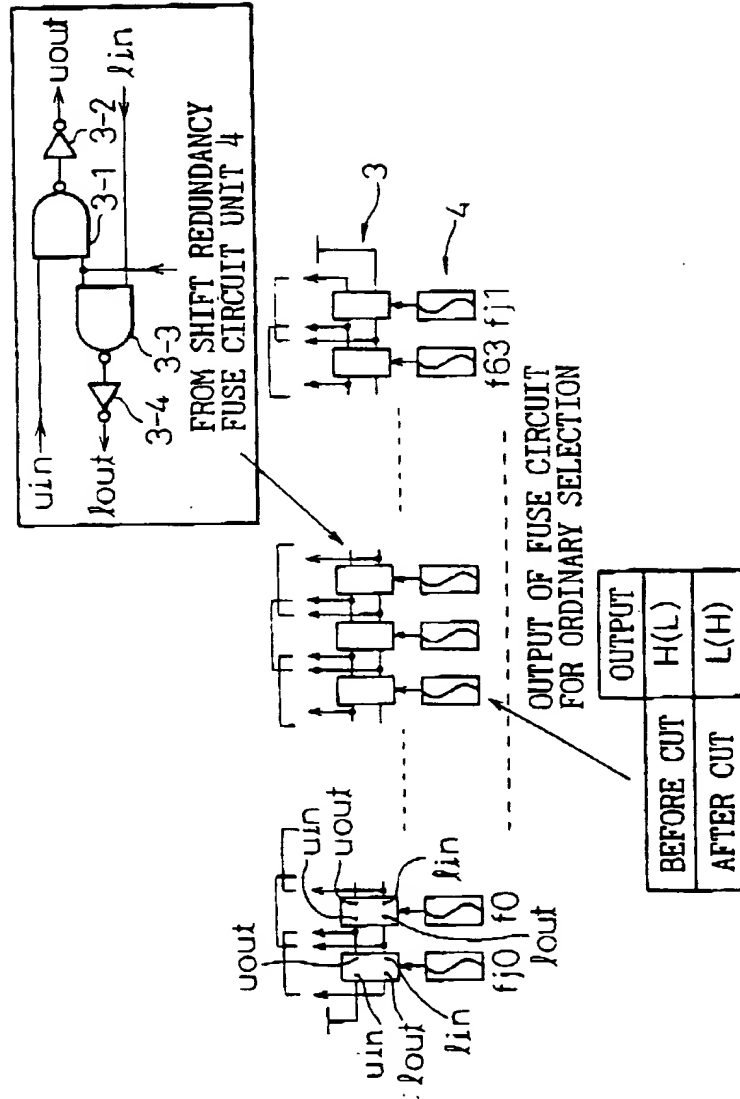
Fig. 3



662240" 29265E60

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Fig. 4



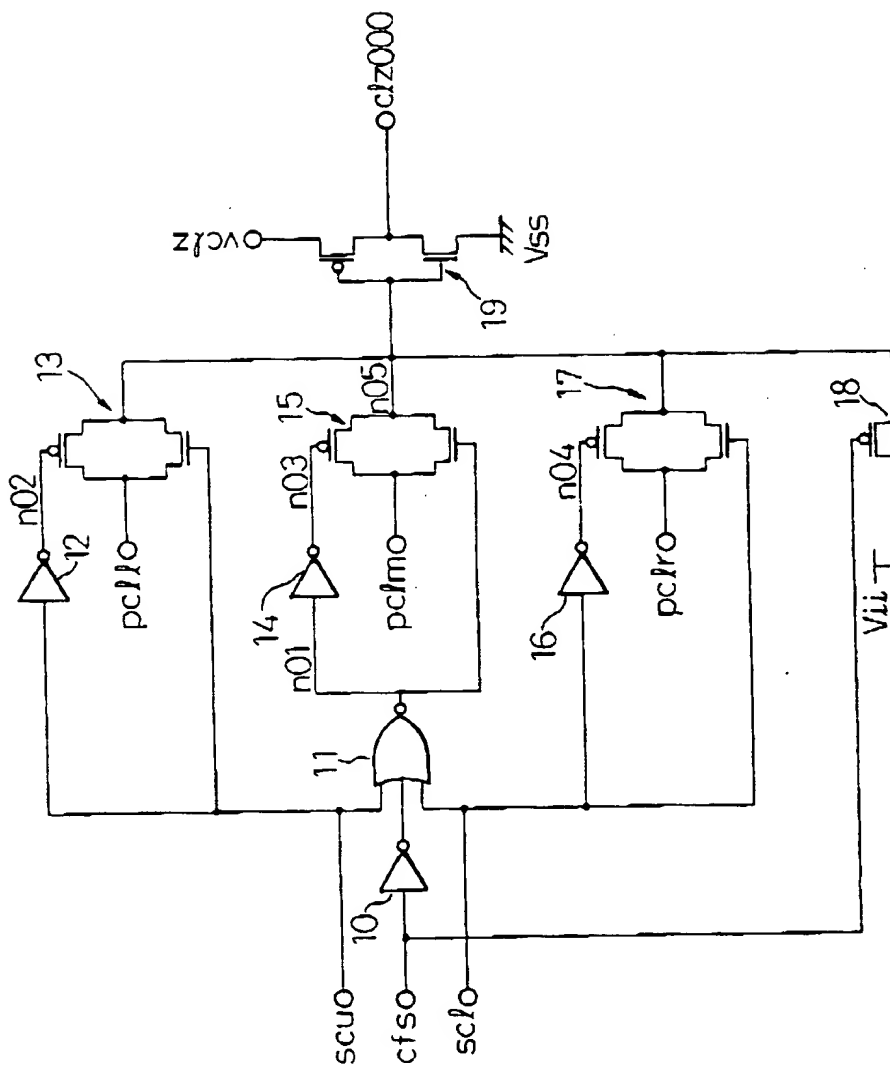
REMARKS)  
"L" OR "H" IN PARENTHESES REPRESENTS OUTPUT OF FUSE  
CIRCUIT FOR REDUNDANCY SELECTION

TWO FUSES BLOW/ONE REDUNDANCY



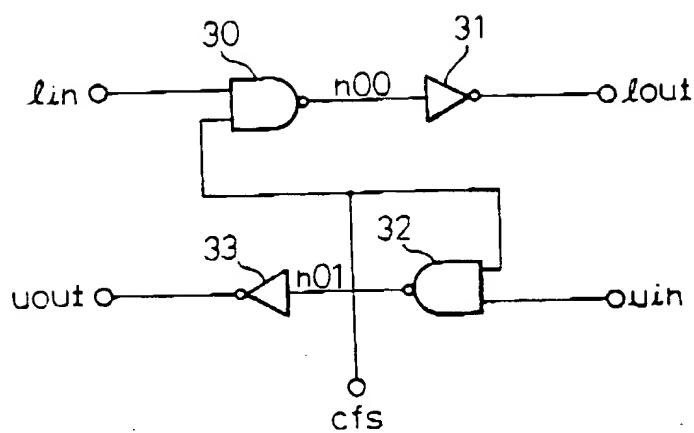
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Fig. 6



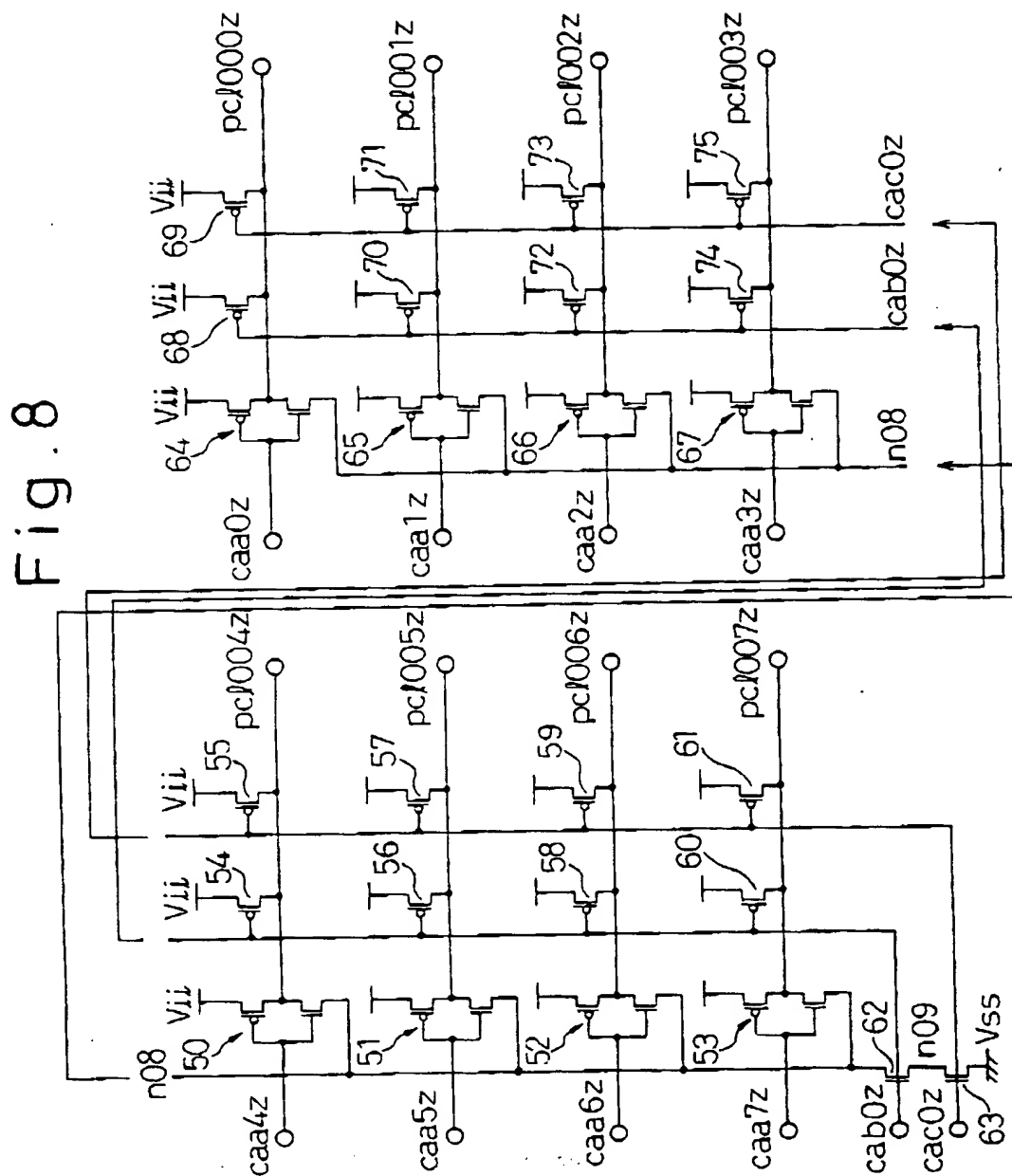
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Fig.7



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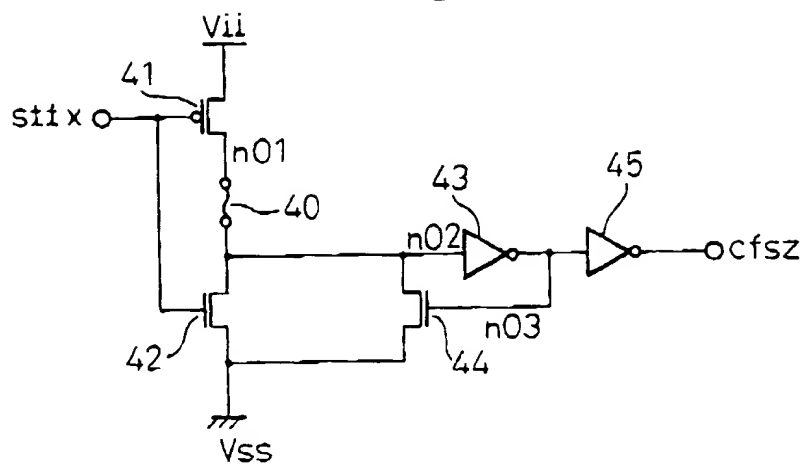


552240" 2946560



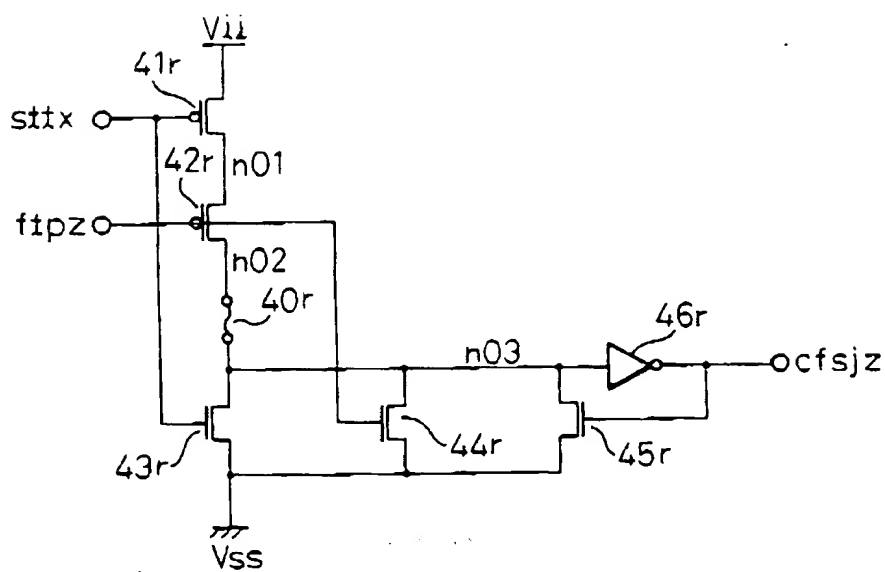
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Fig.9



( USED FOR SELECTING LINES  
cl0,cl2 TO cl61 AND cl63 )

Fig.10

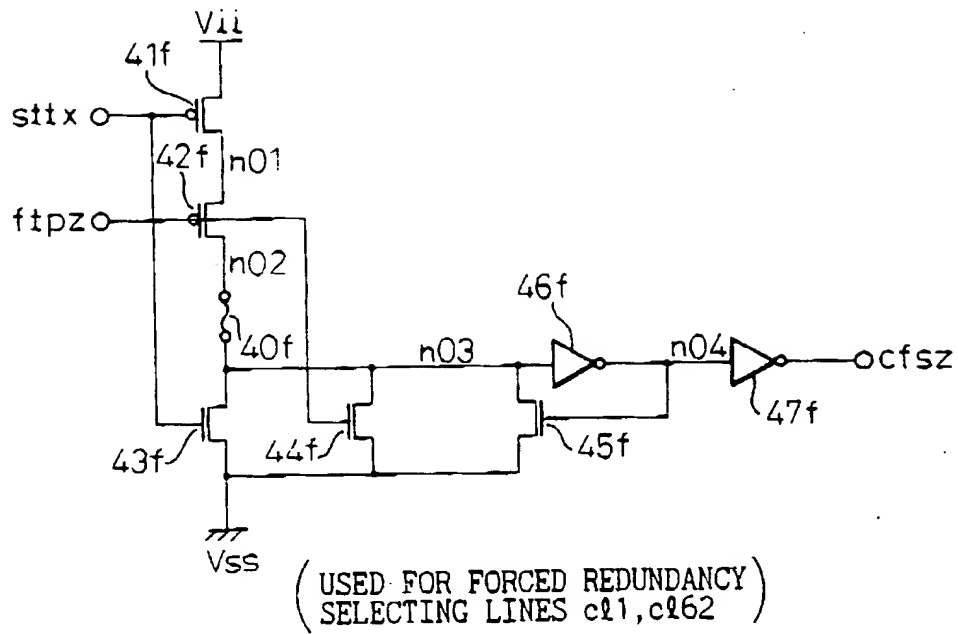


( USED FOR REDUNDANCY  
SELECTING LINES clj0,clj1 )

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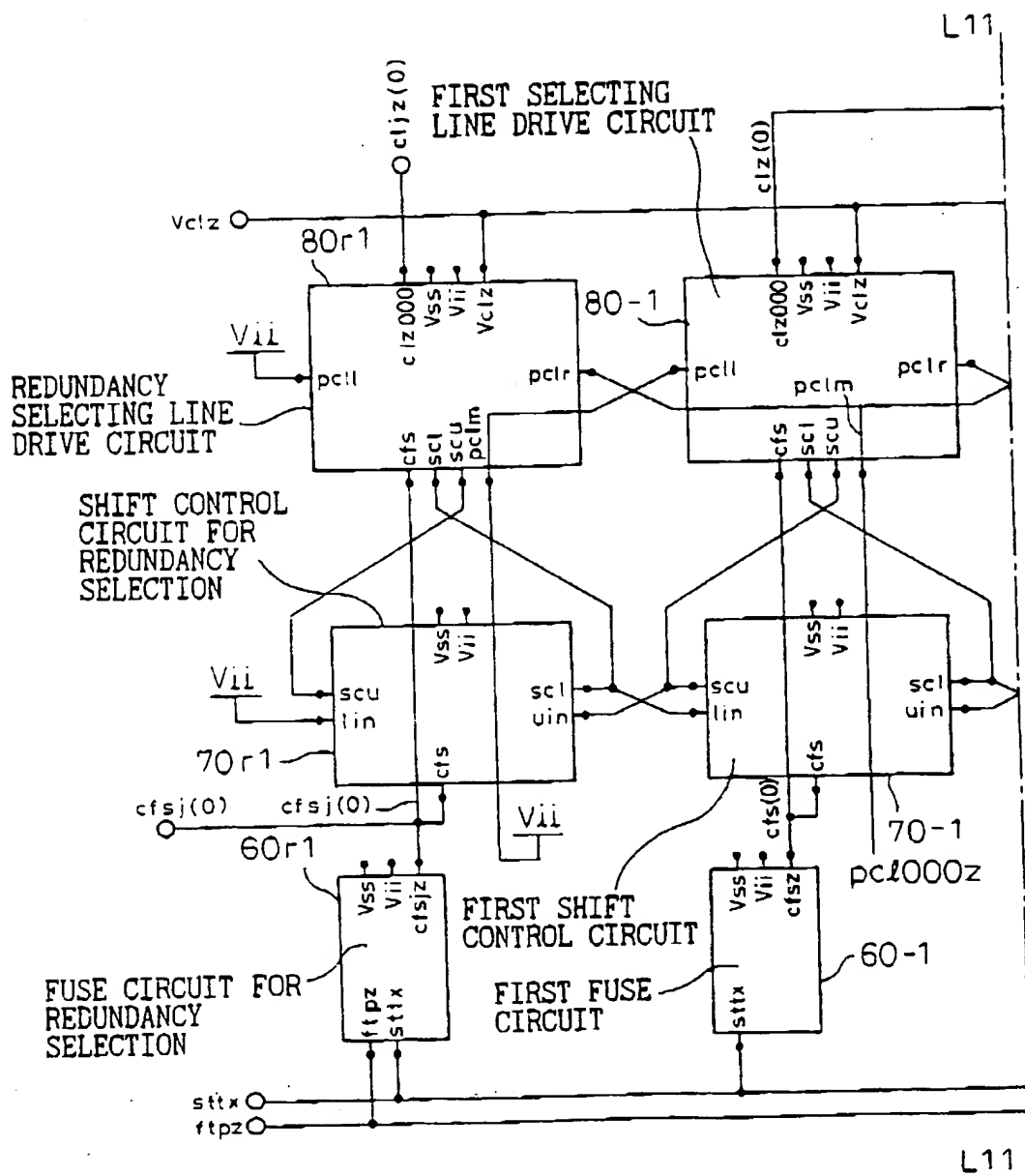
Fig.11



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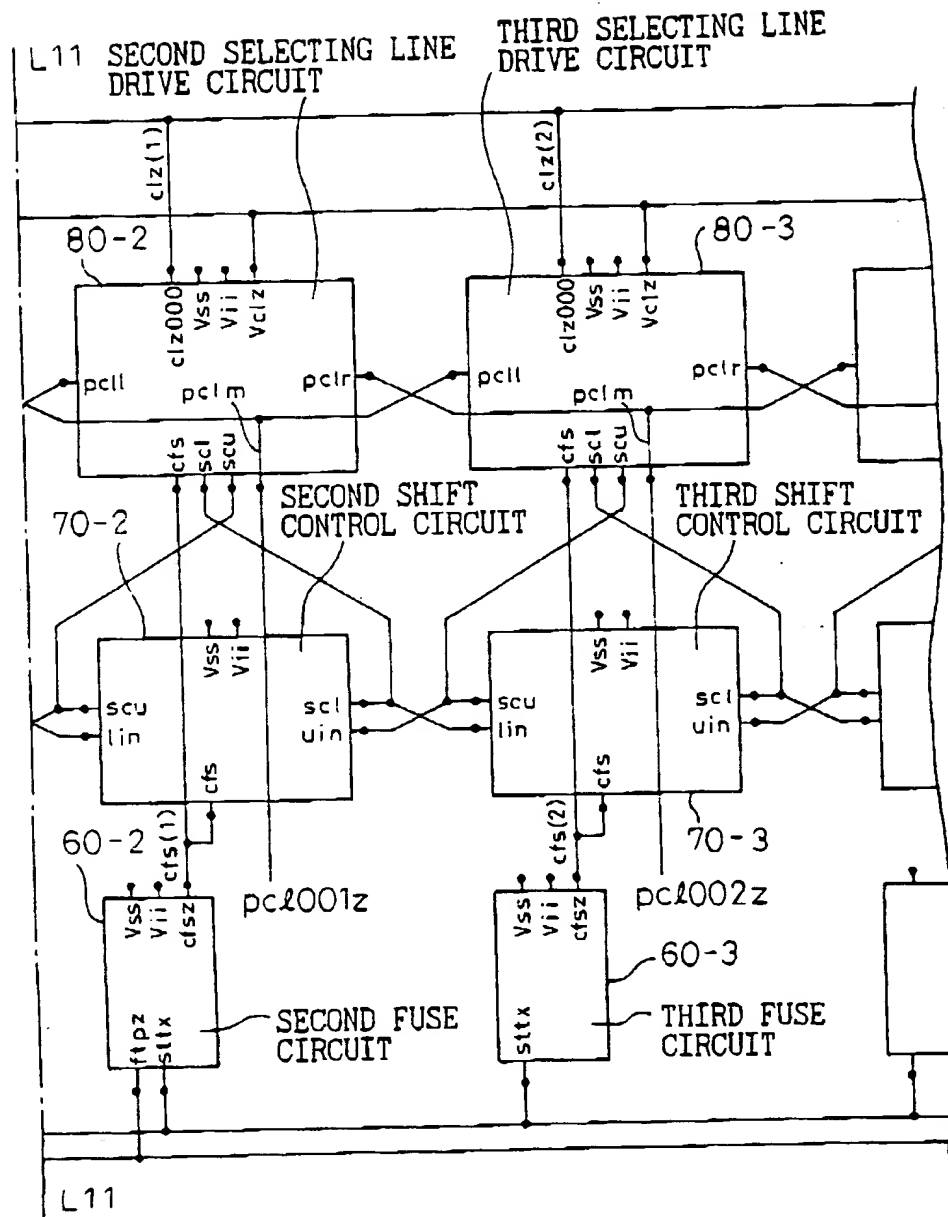
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Fig. 12



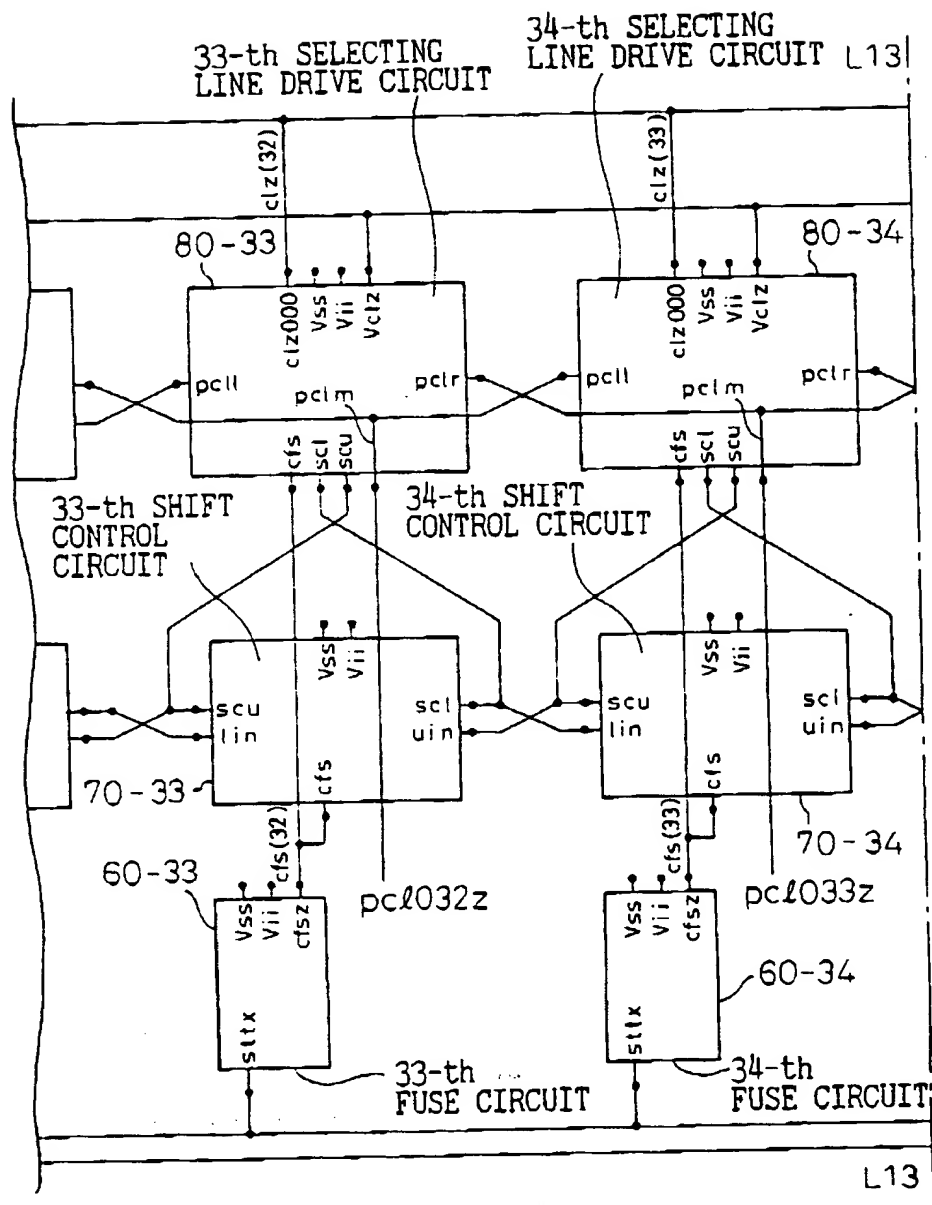
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Fig. 13



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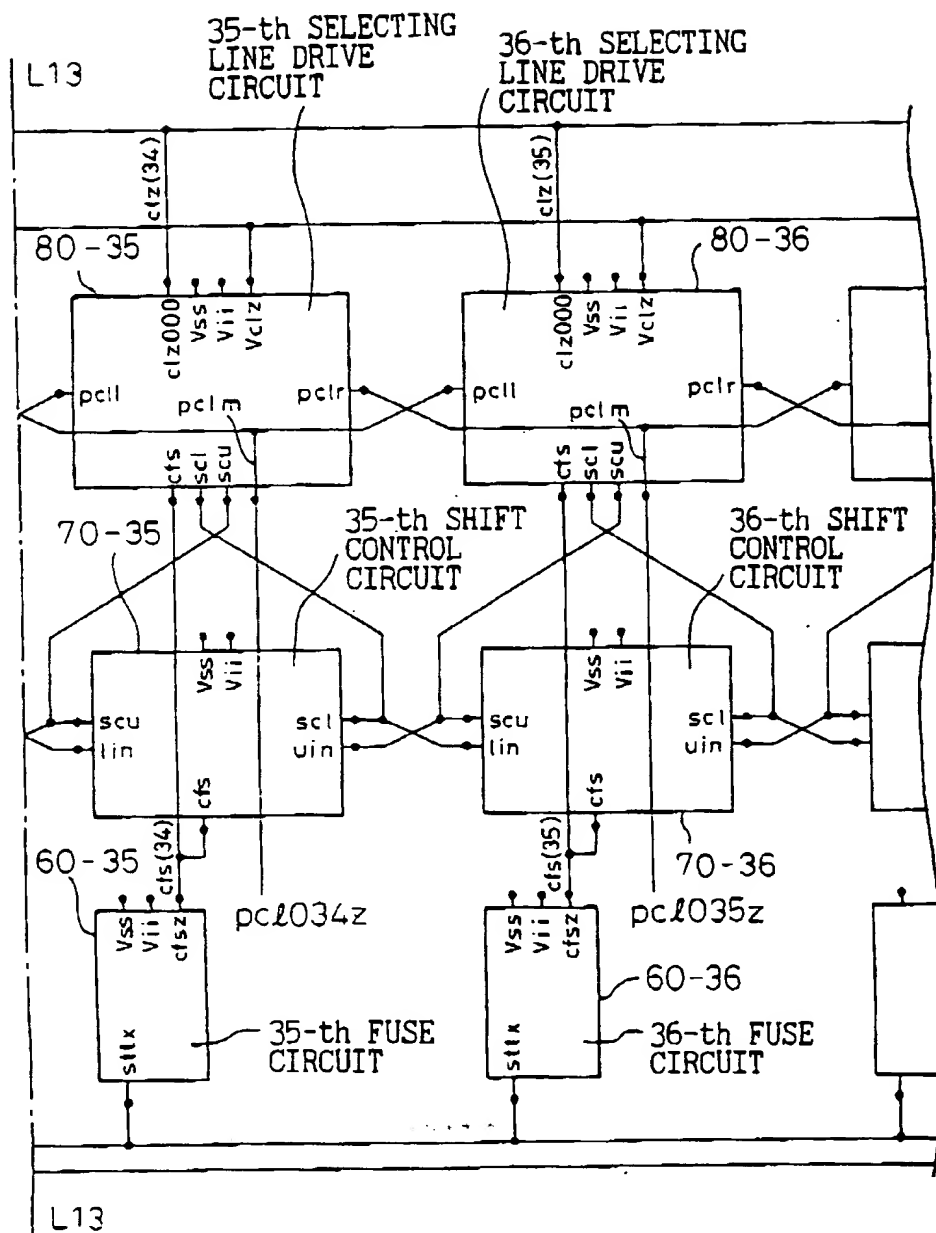
Fig.14



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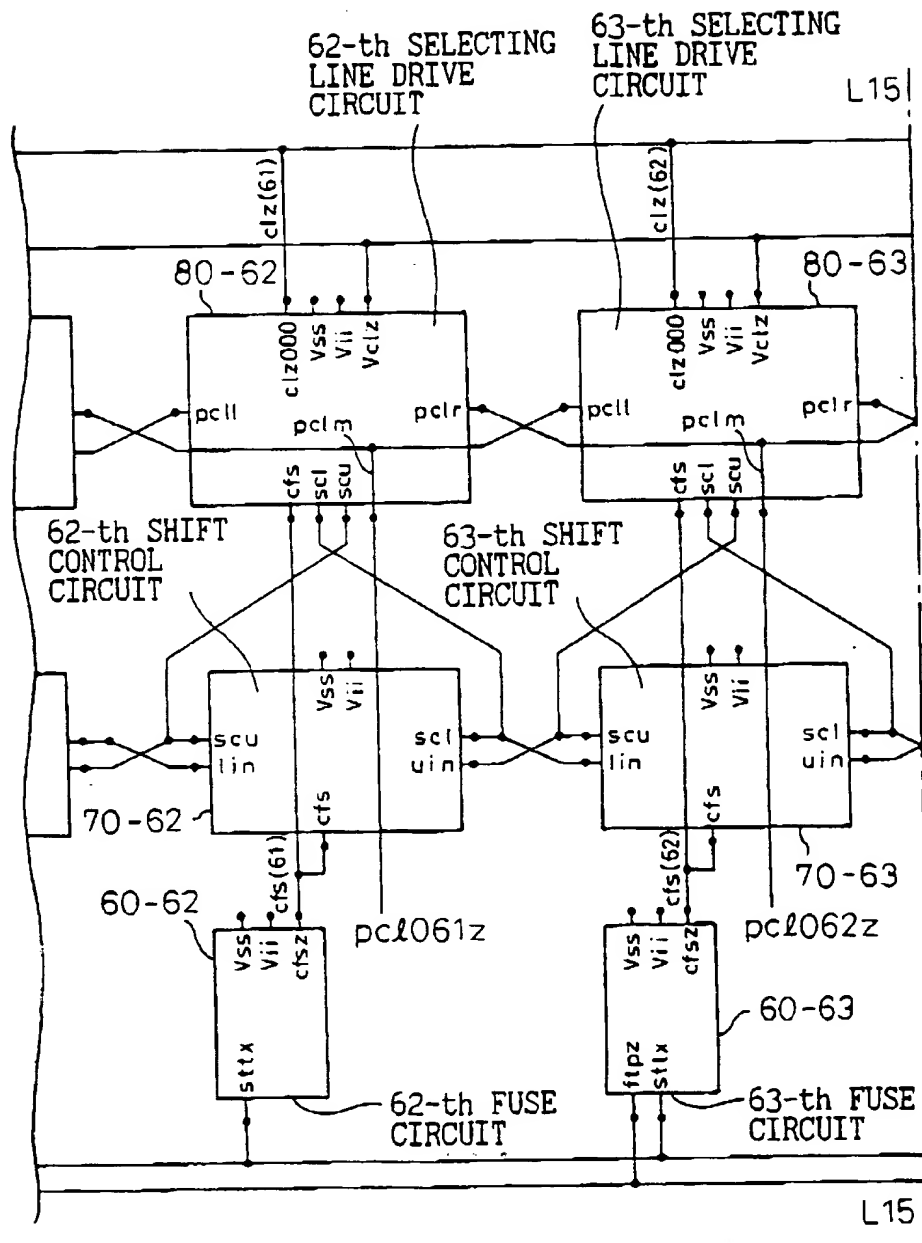
Fig.15



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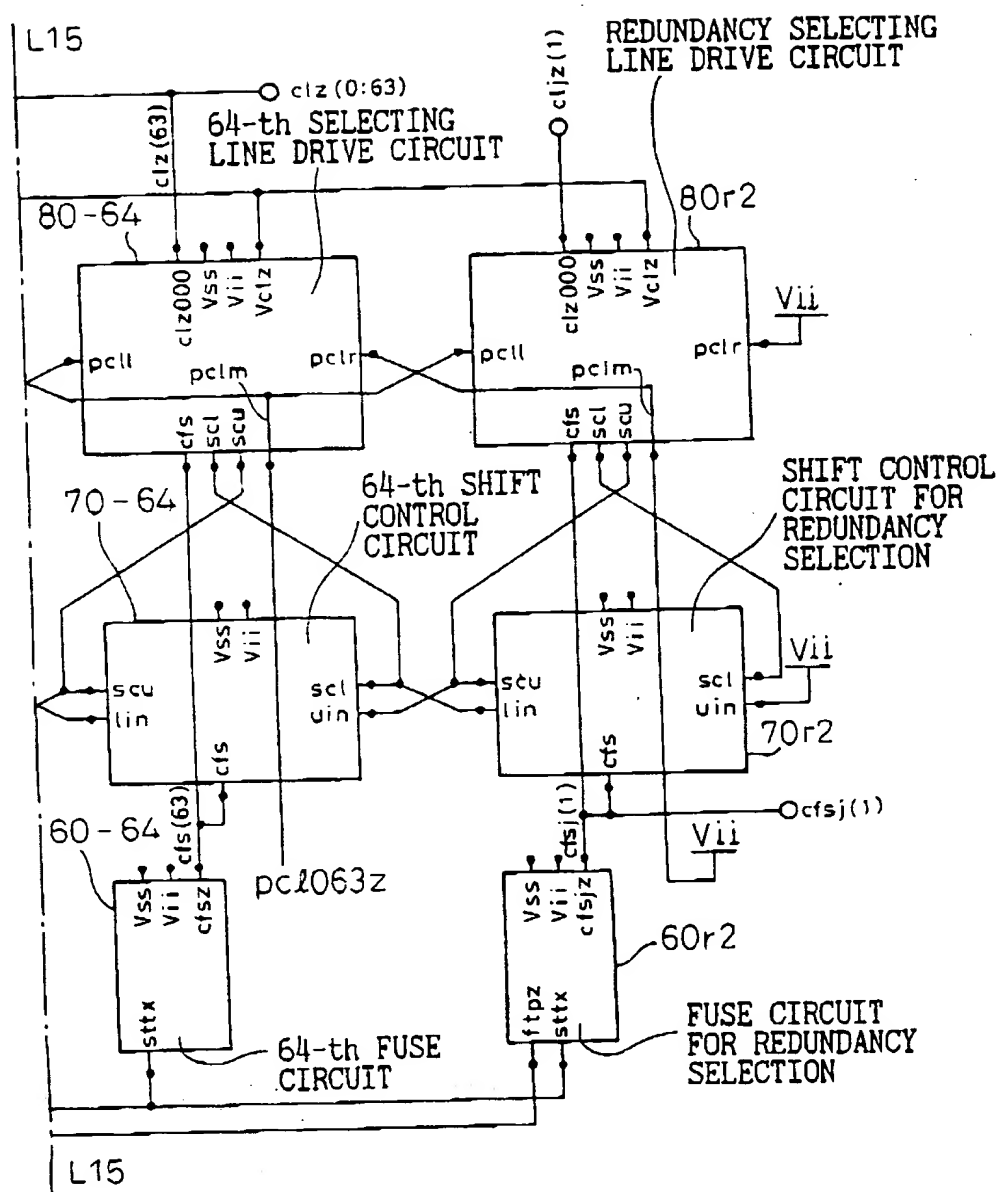
Fig.16



662220" 29465560

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Fig.17

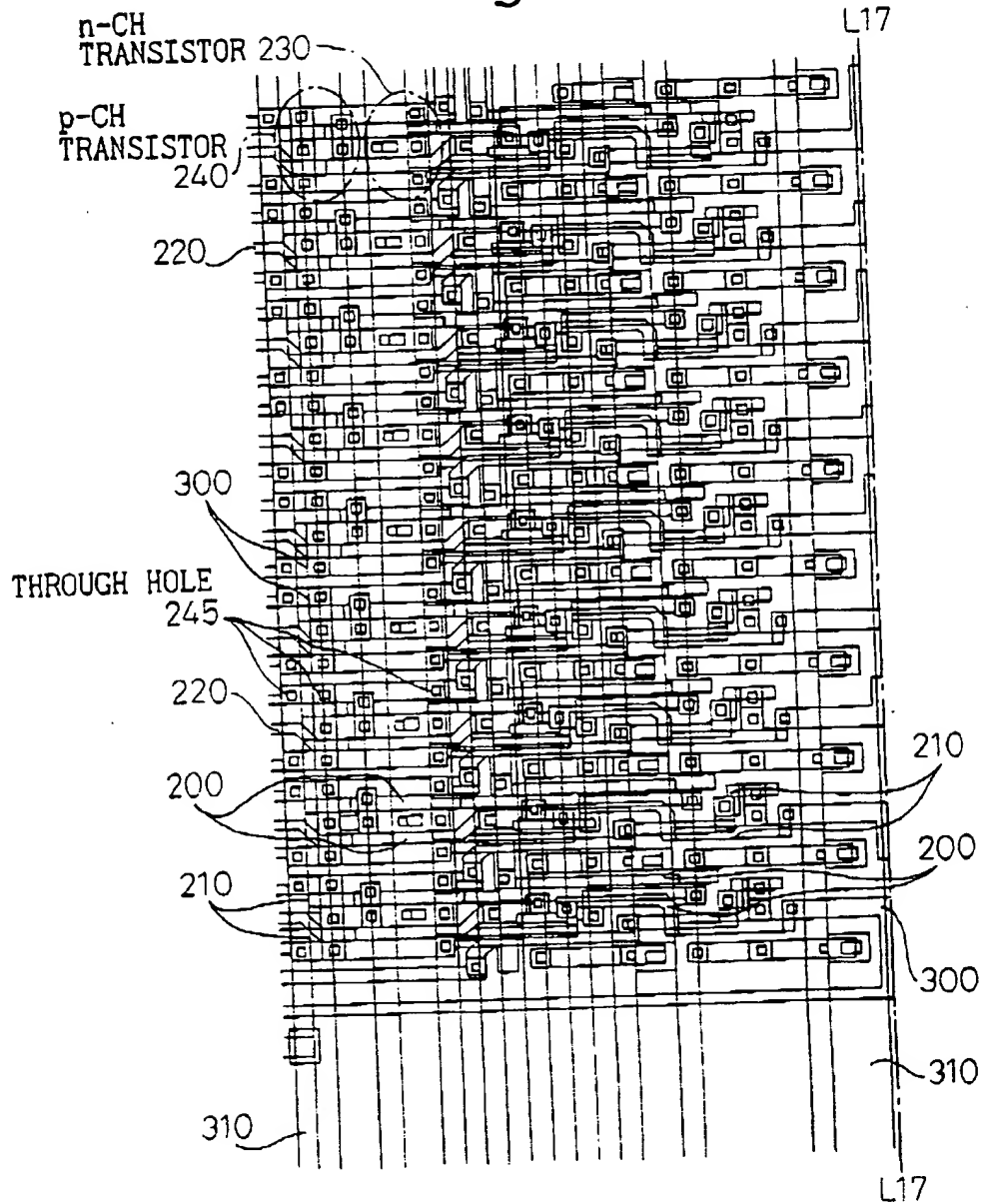


S62220" 29265E60



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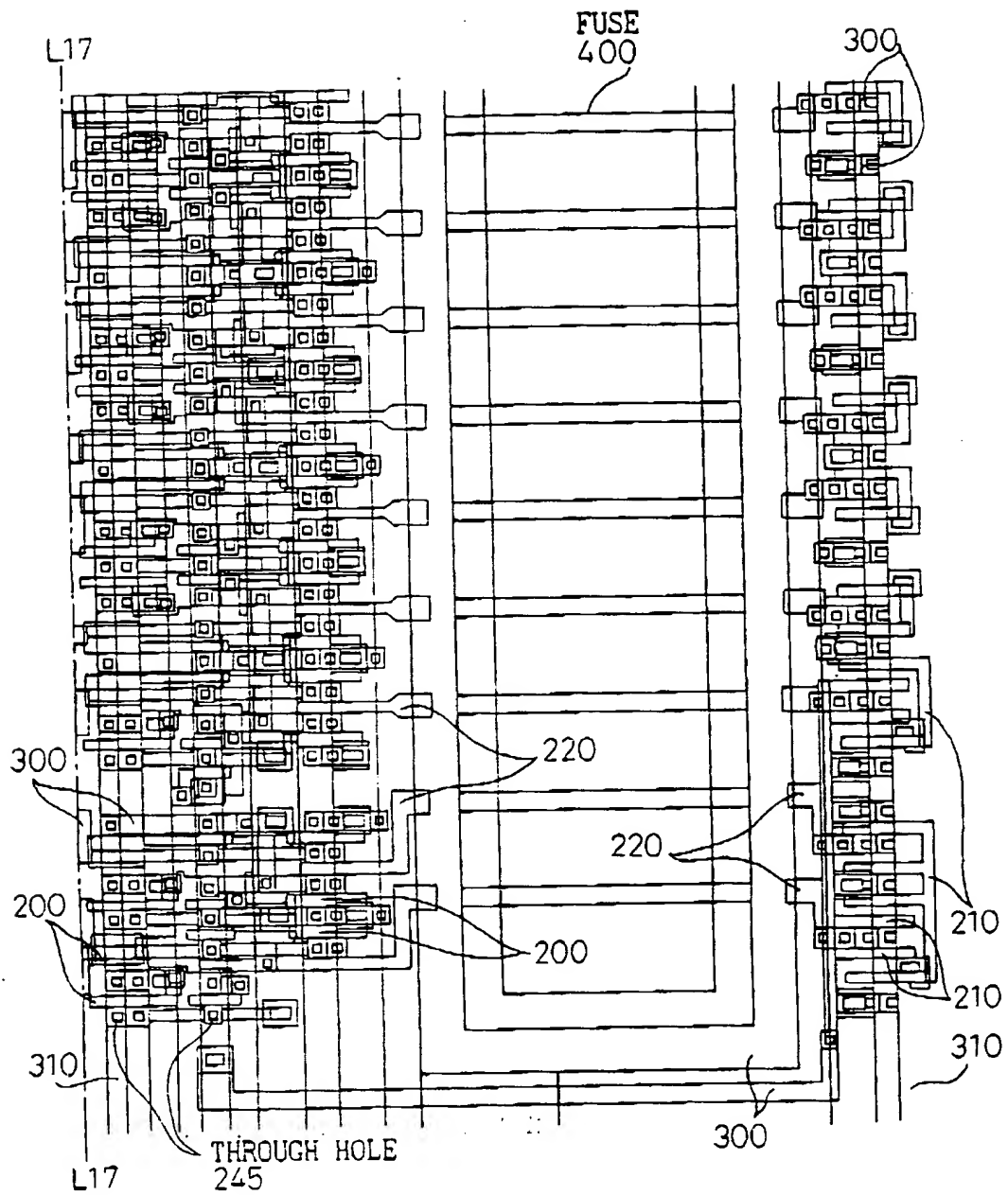
Fig.18



002240" 29265E60

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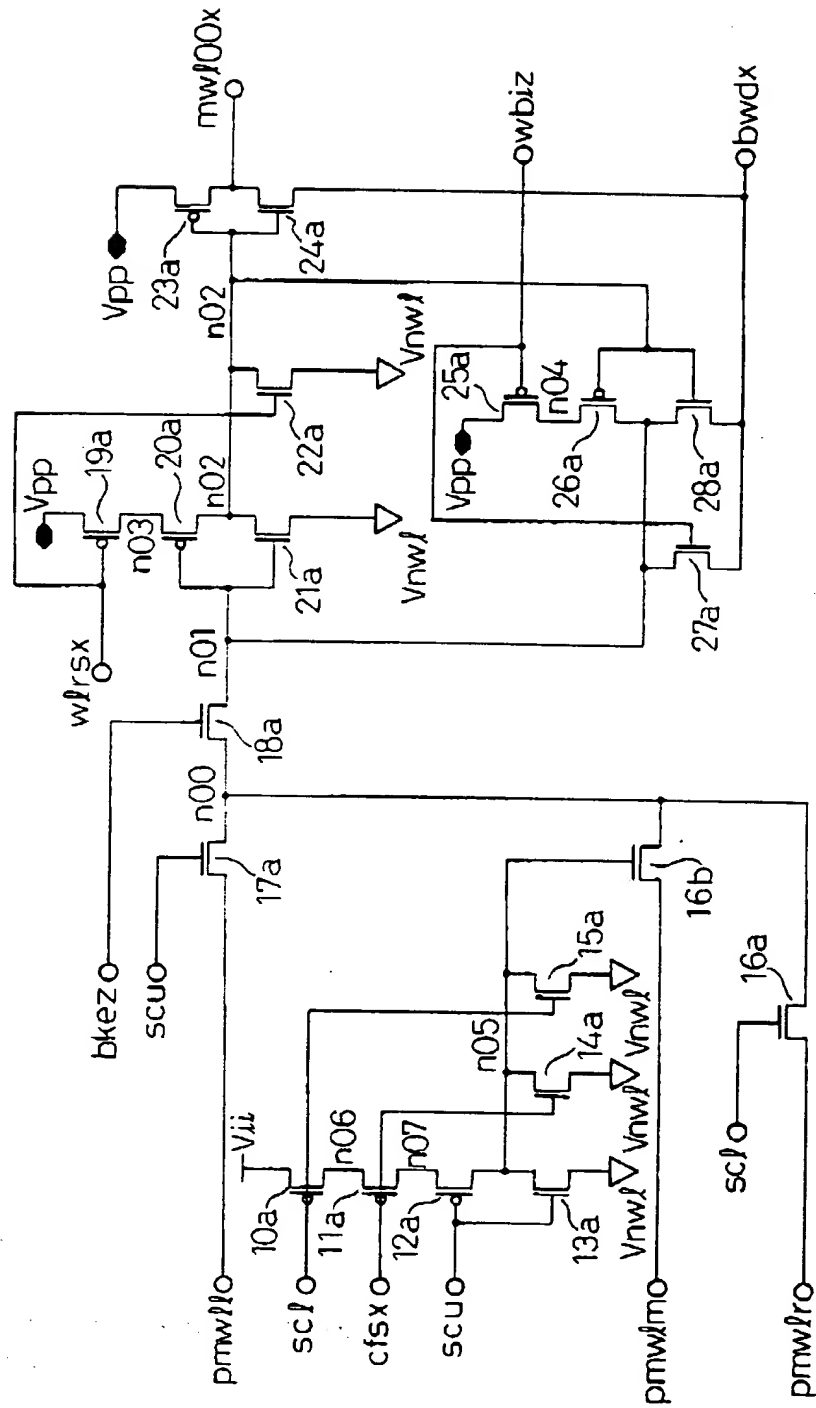
Fig.19



002220" 29265E60

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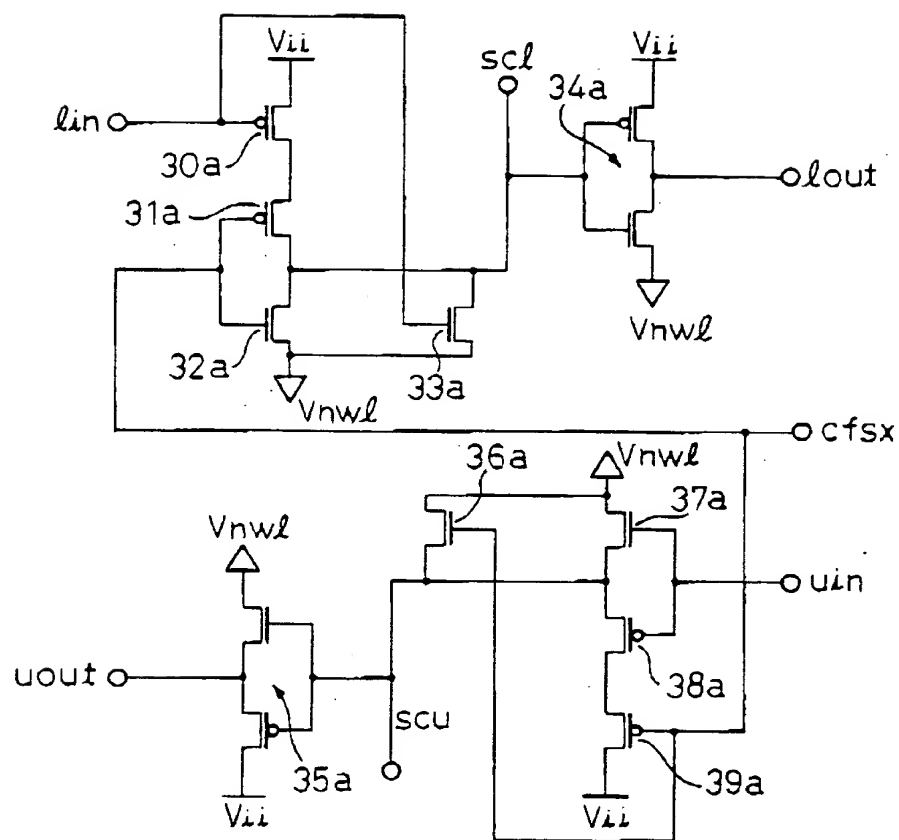
Fig. 20



662220 49265E60

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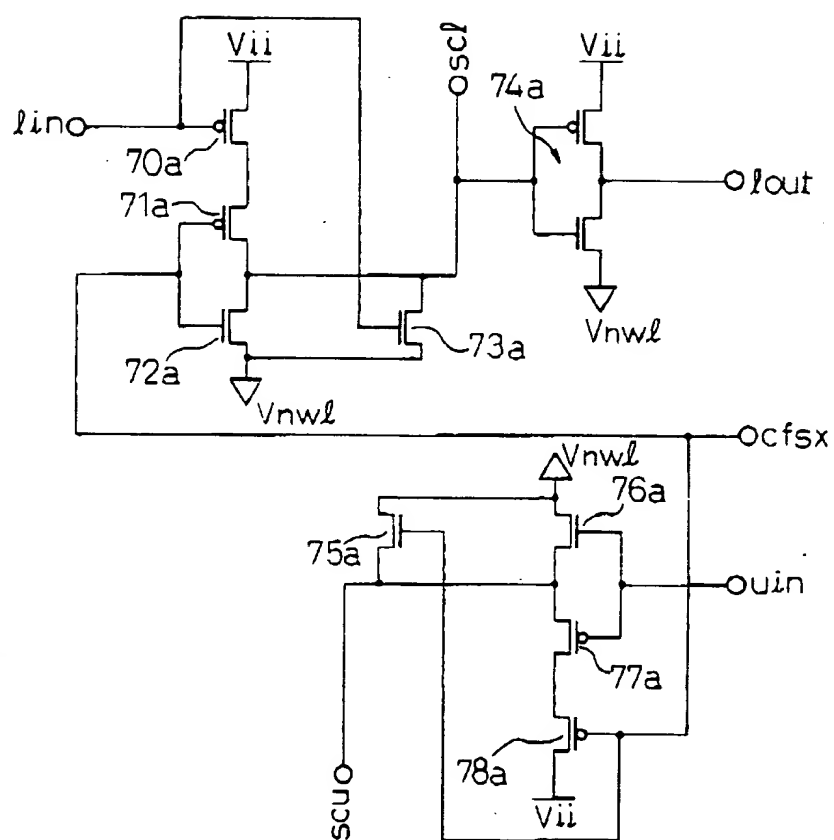
Fig. 21



652220" 2965660

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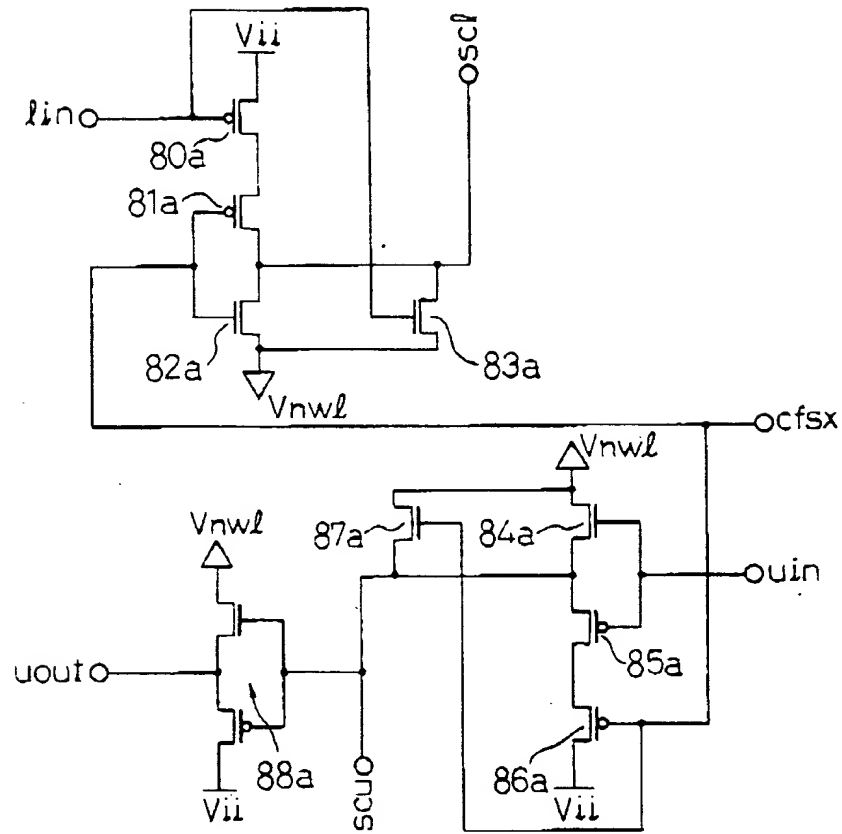
Fig. 22



002240" 29265E60

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Fig. 23



652220" 49265E60

$$\frac{23}{87}$$

Fig. 24

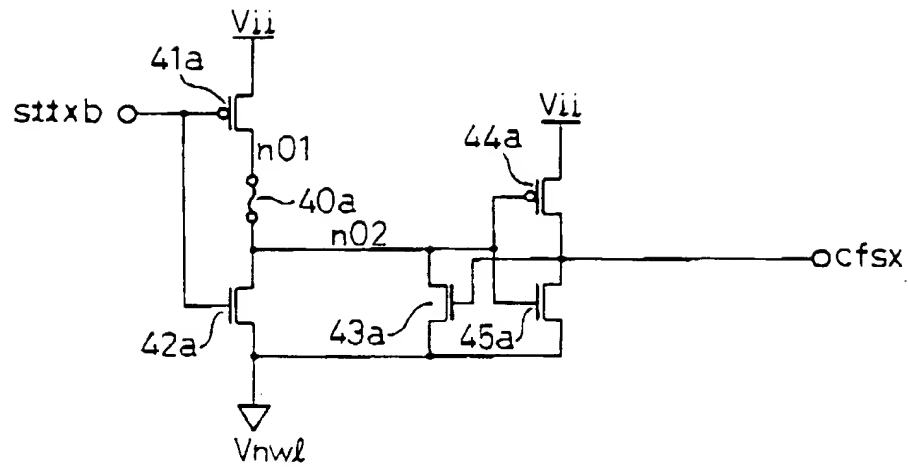
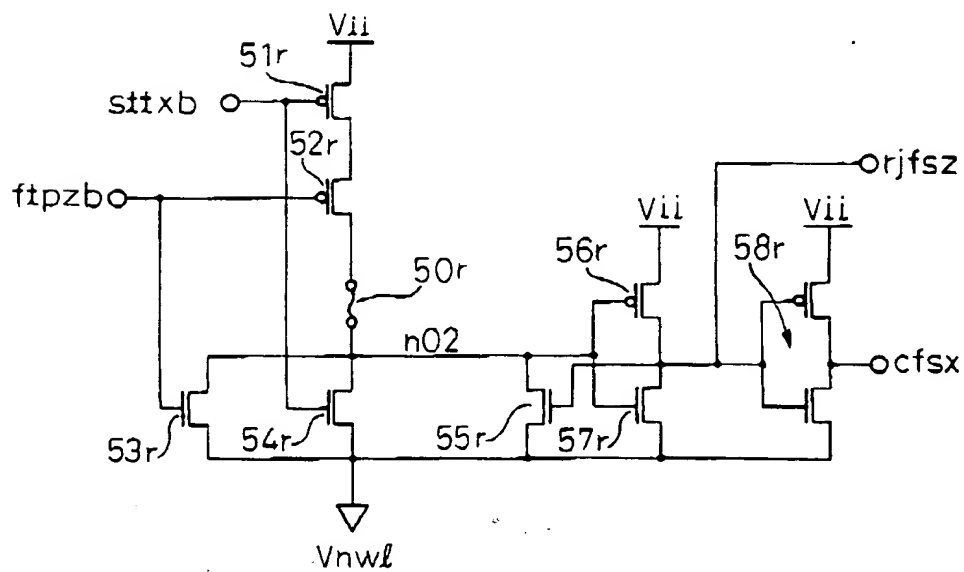


Fig. 25



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Fig.26

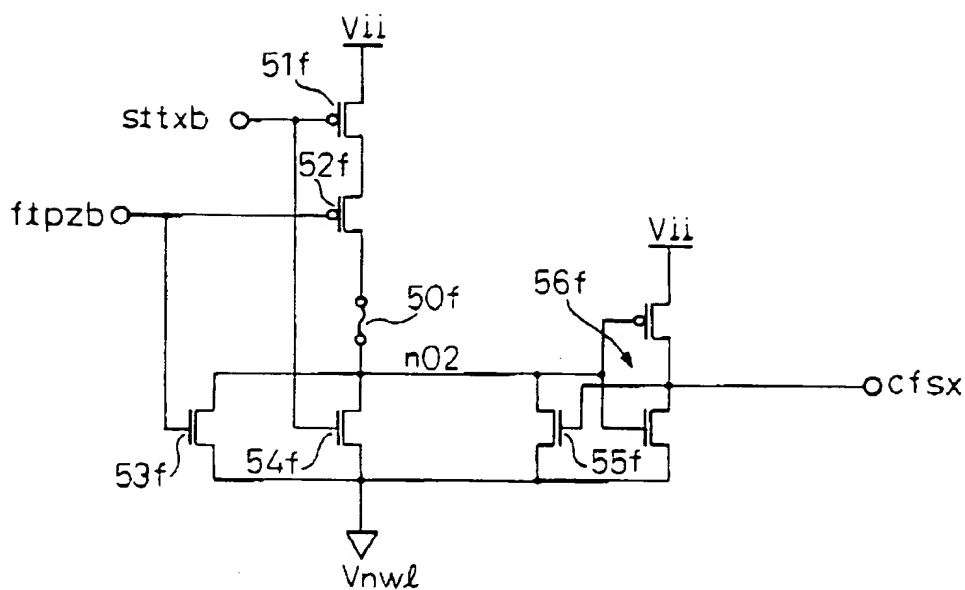
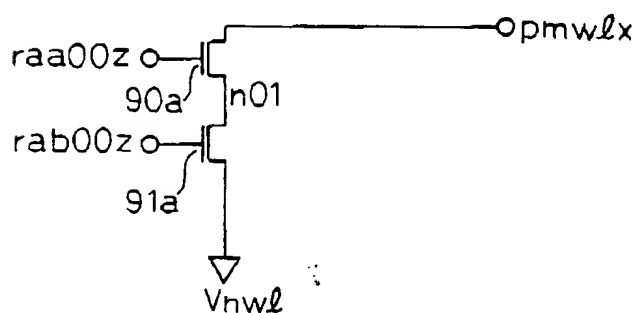


Fig.27





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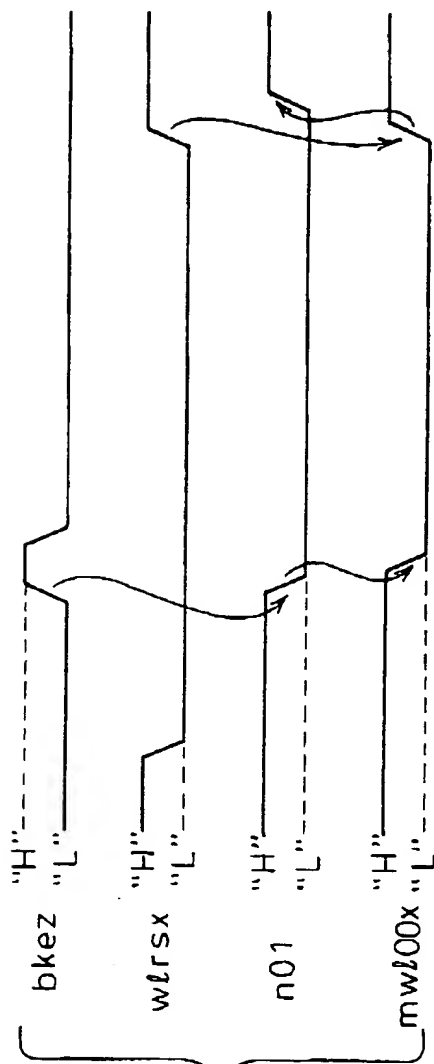


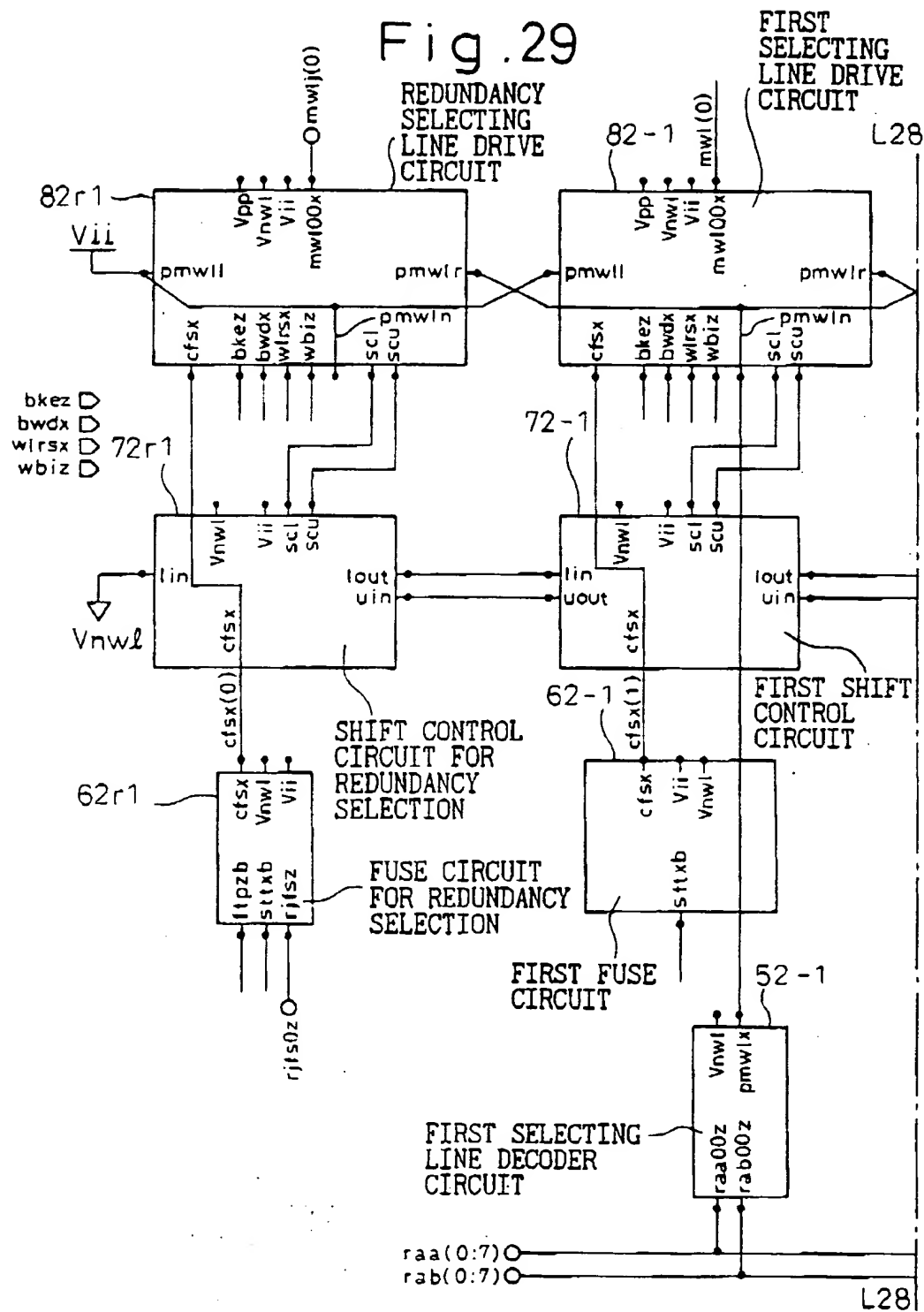
Fig. 28

wb2z="L"  
bw dx="L"

552220" 2946560

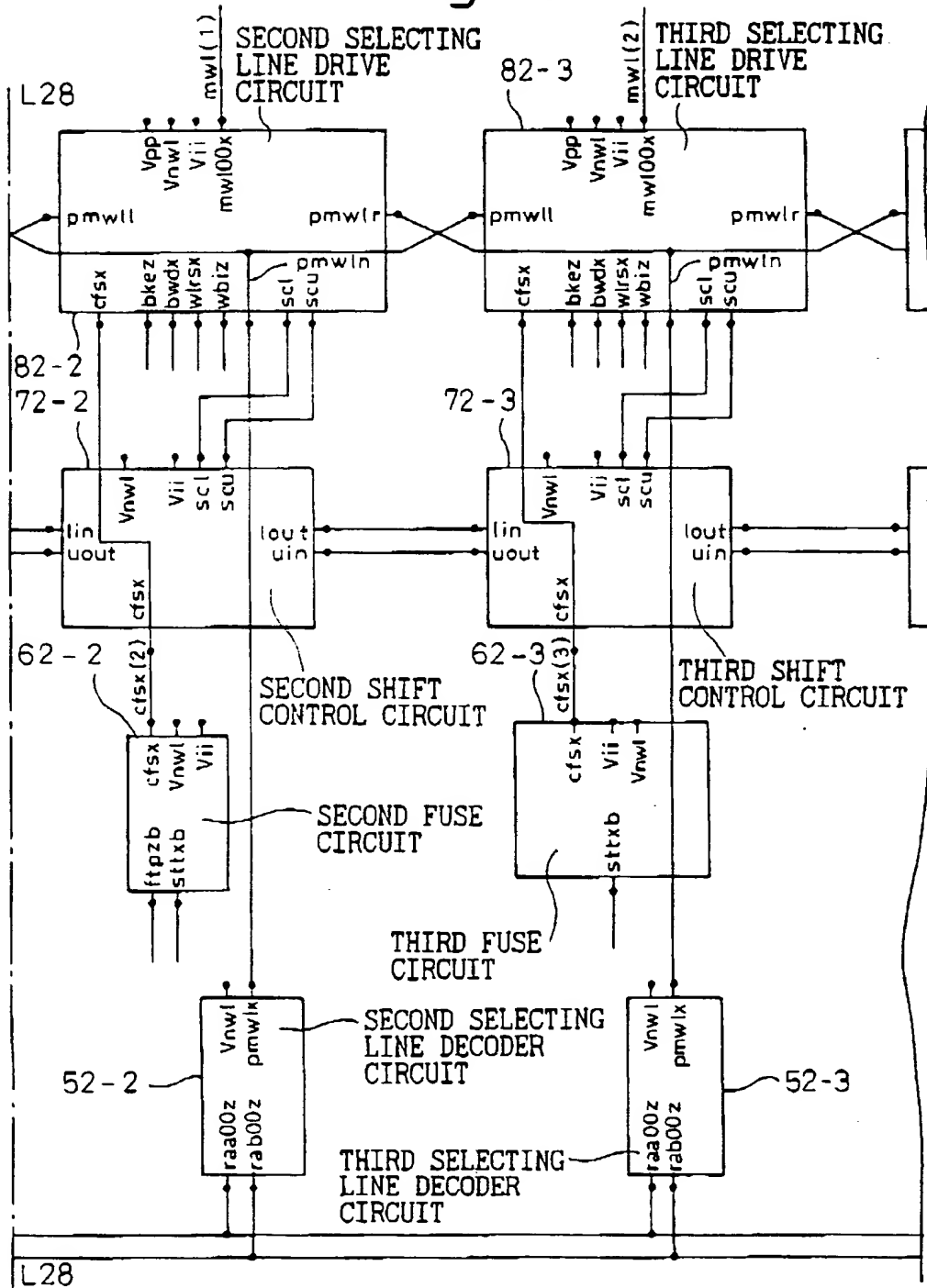
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Fig. 29



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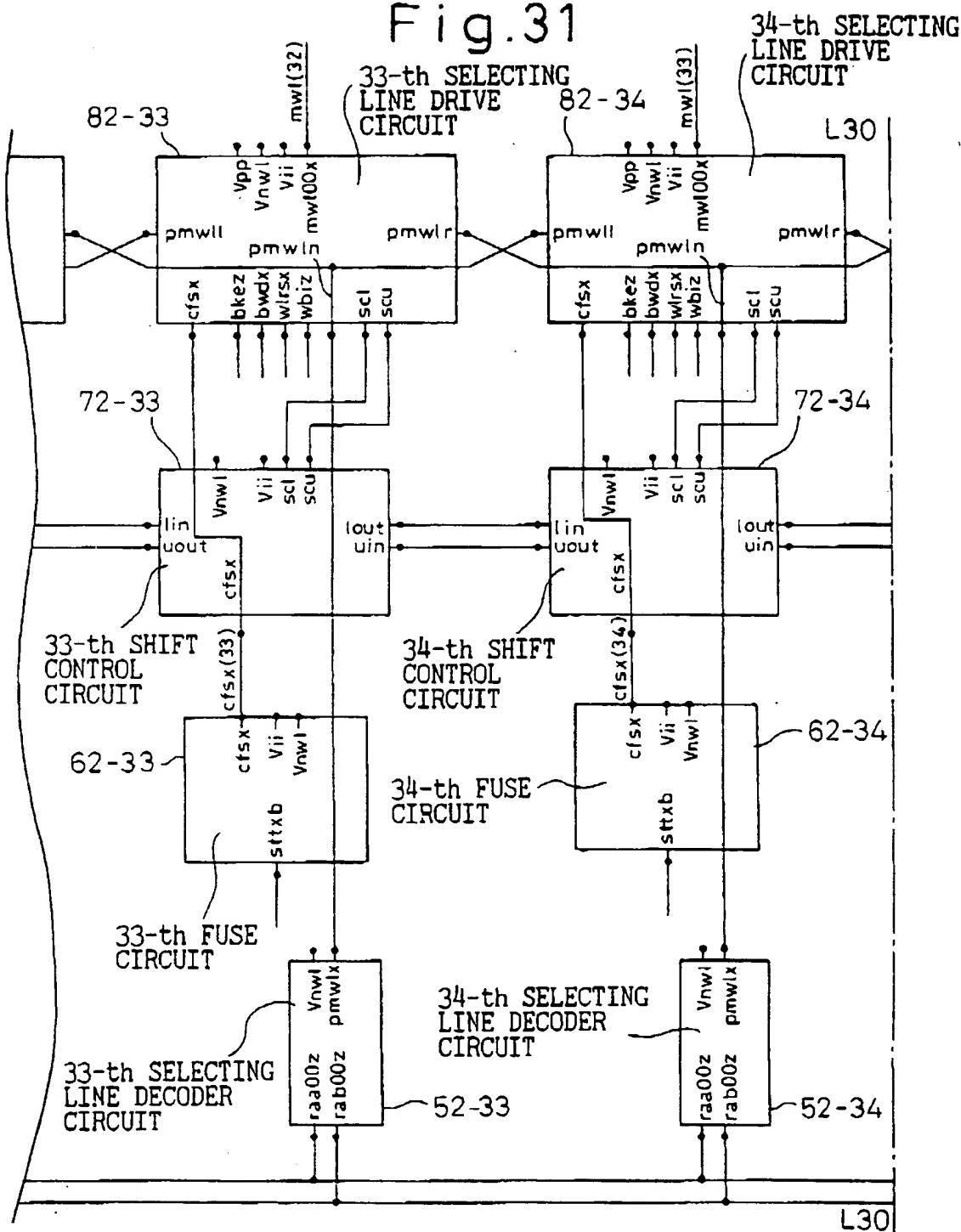
Fig. 30



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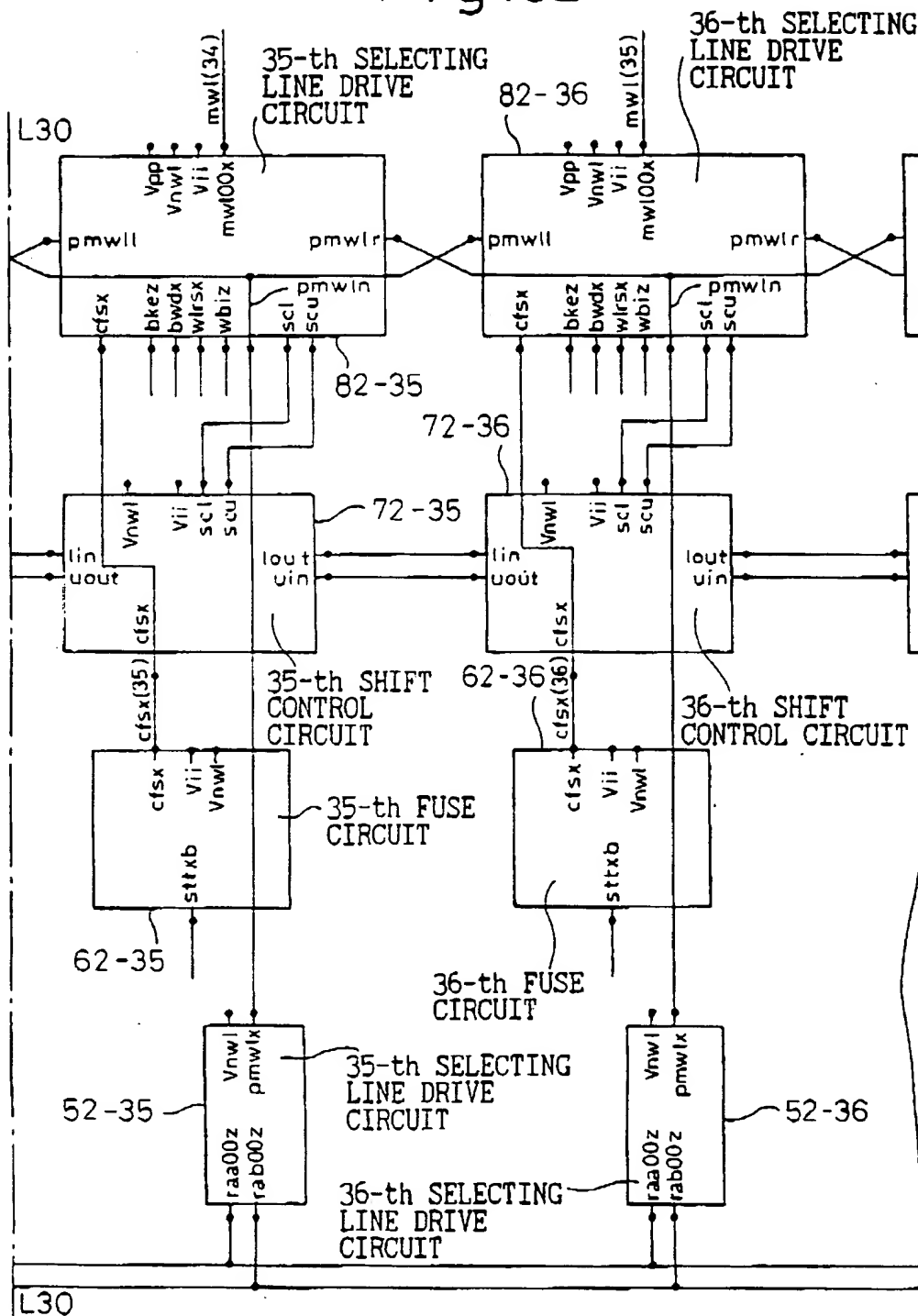
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Fig.31



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Fig. 32

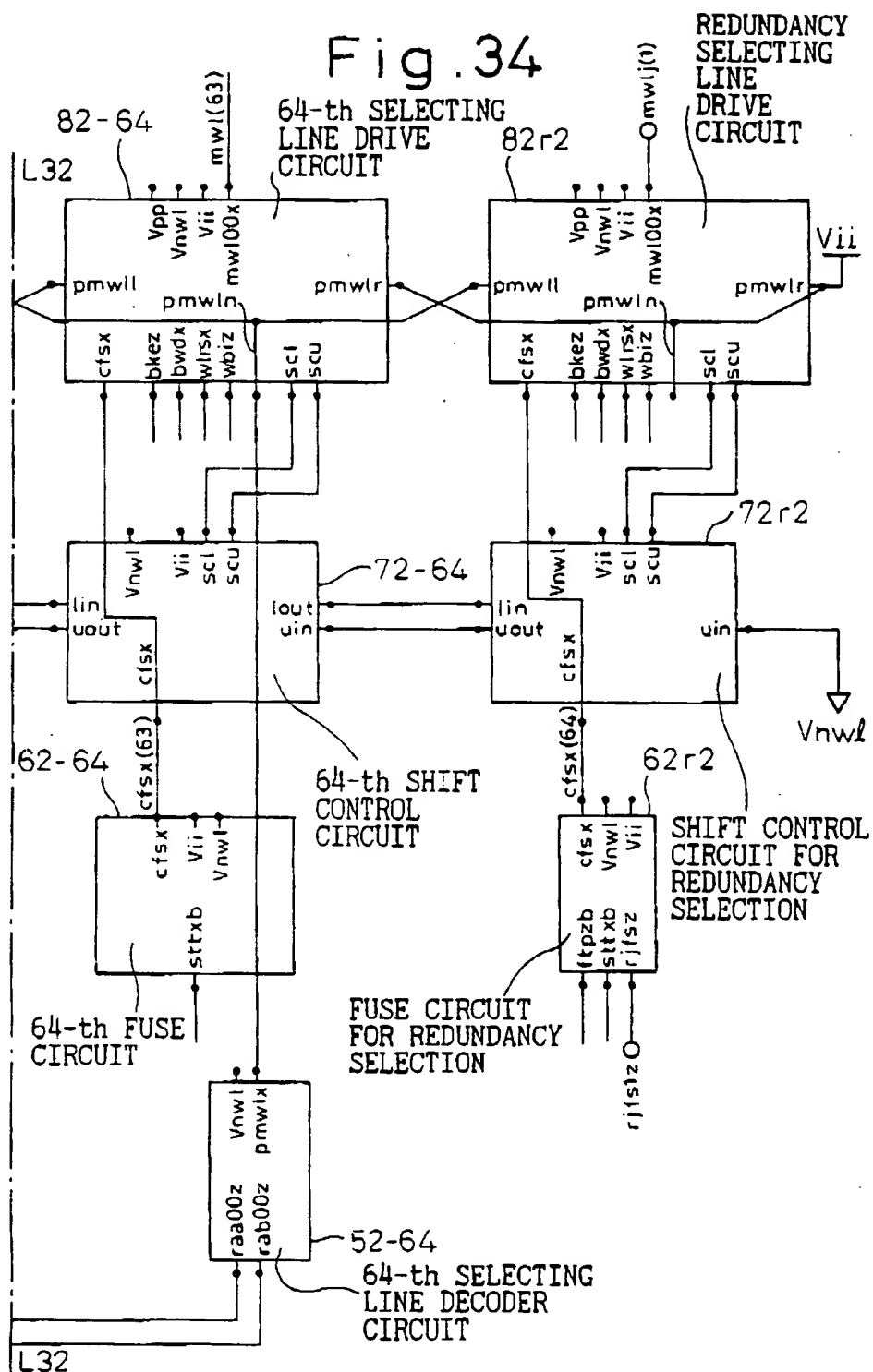


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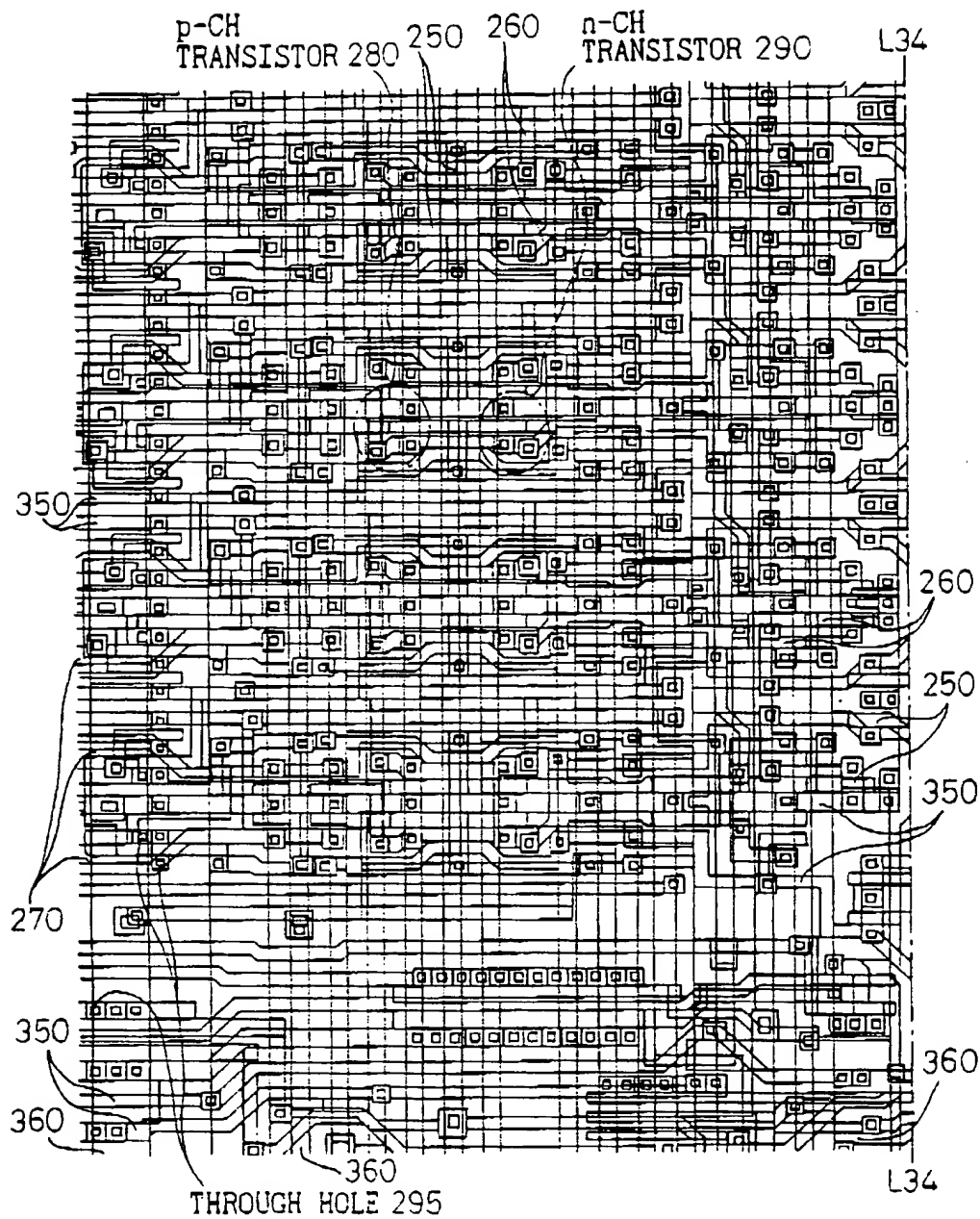
Fig. 34



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Fig. 35

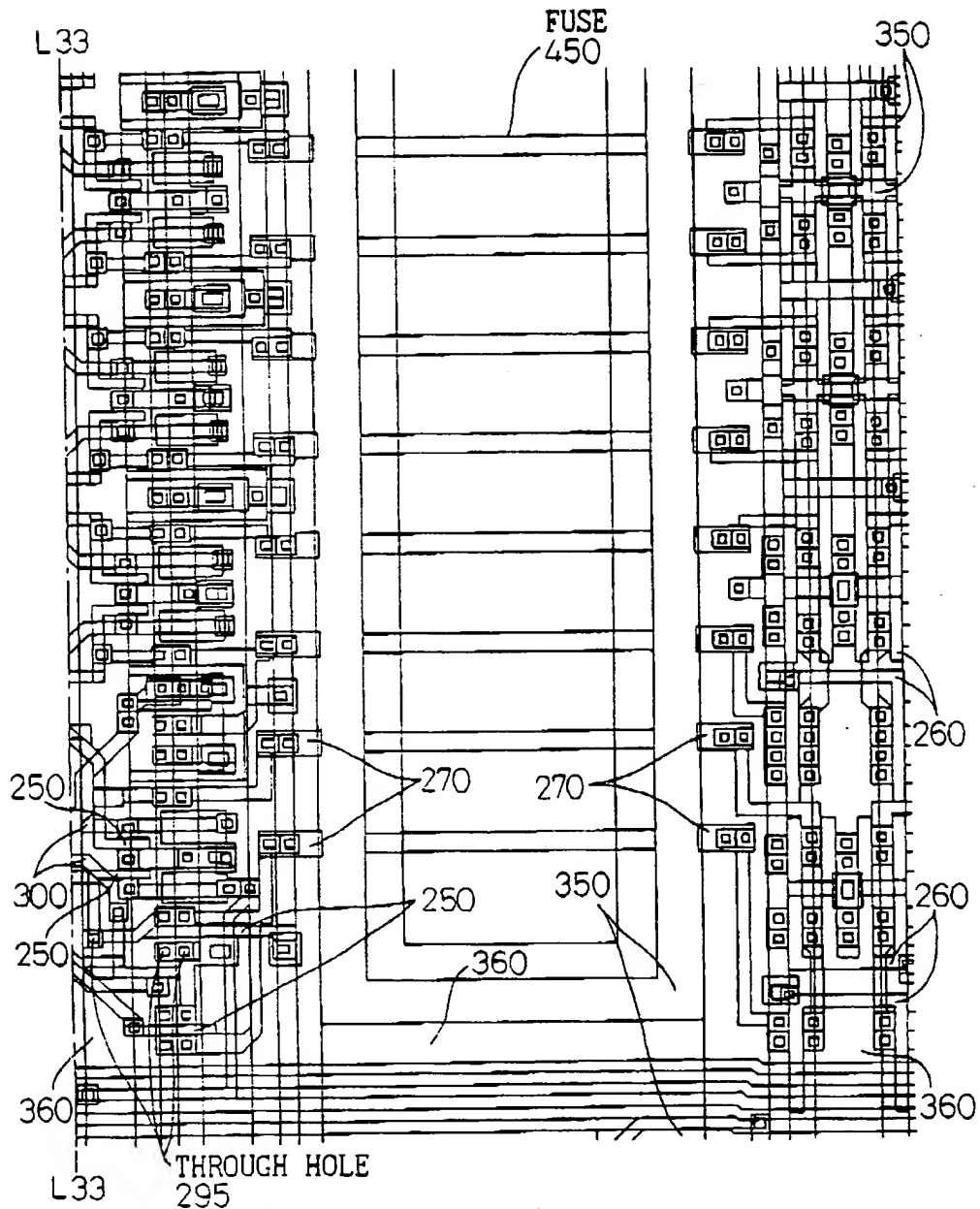


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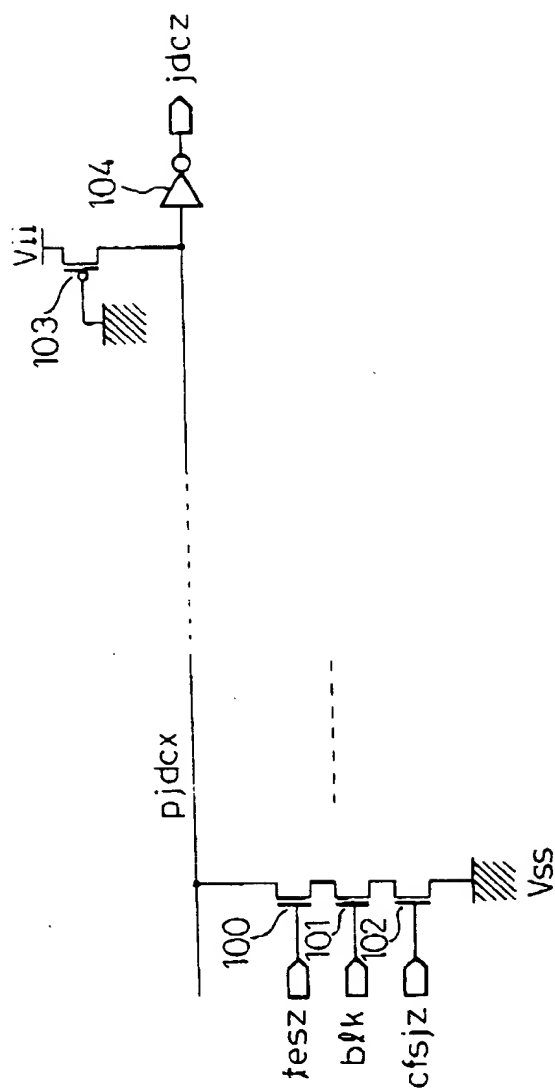
Fig. 36



0935976.0220" 29/65E60

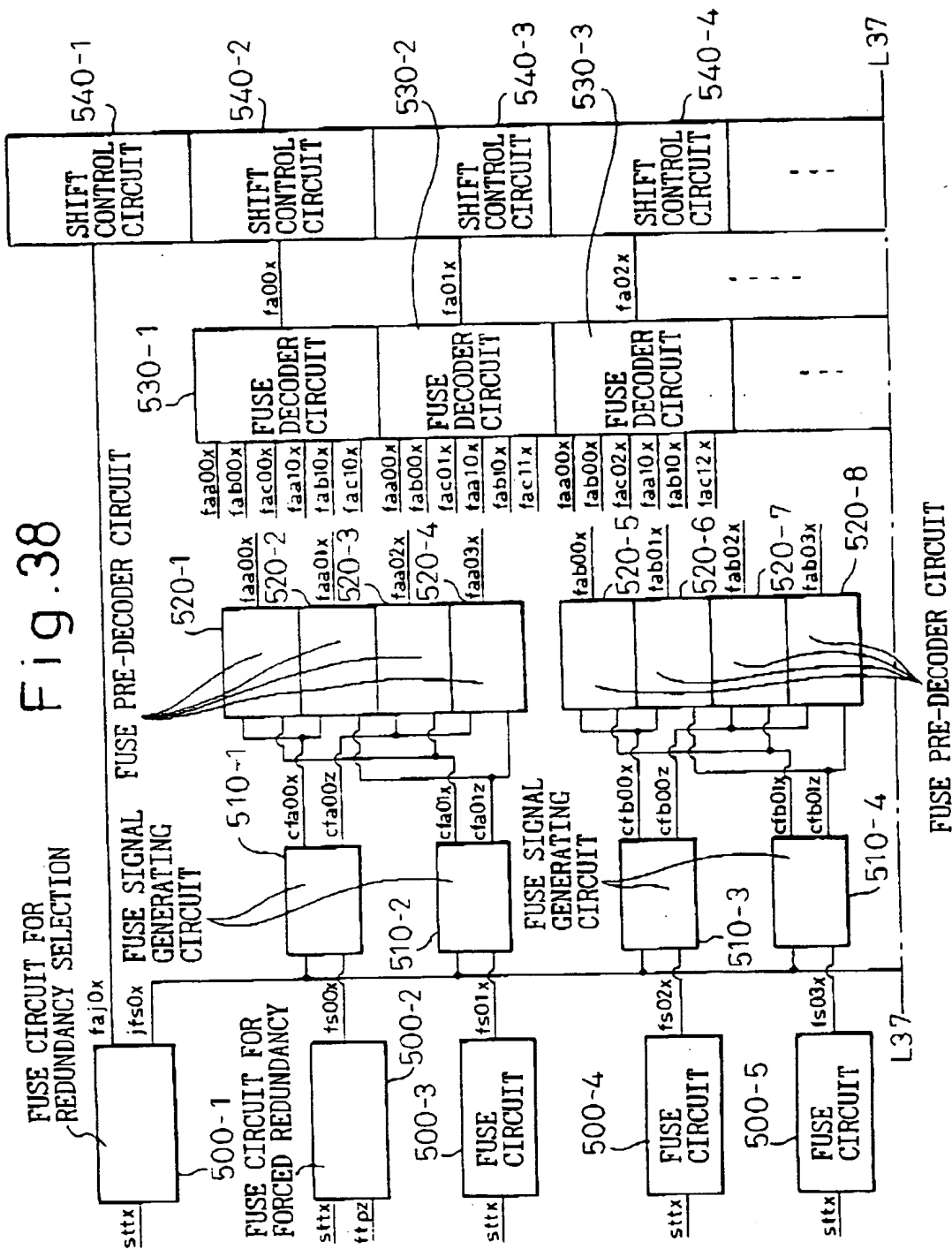
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Fig. 37



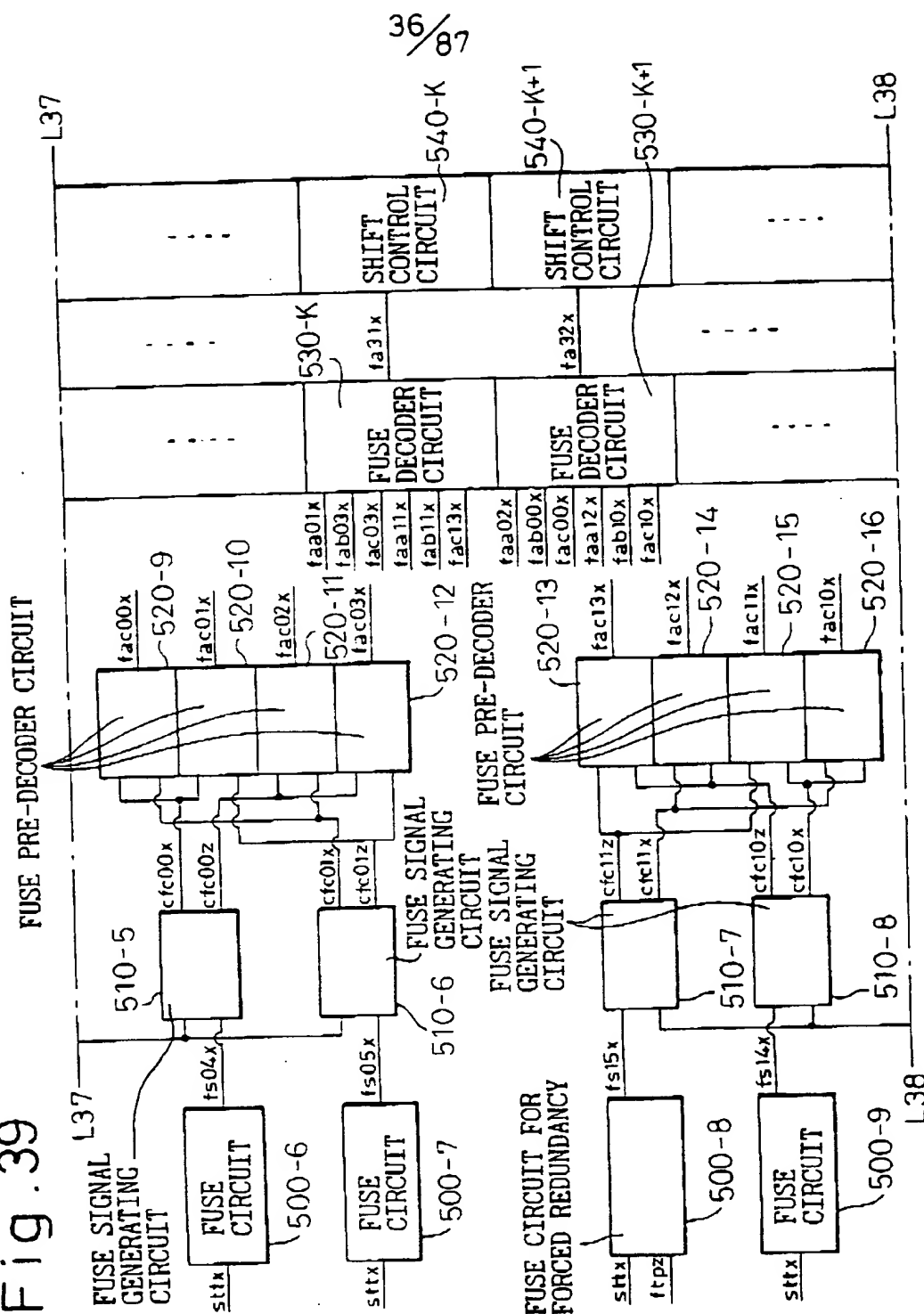
552240" 2925560

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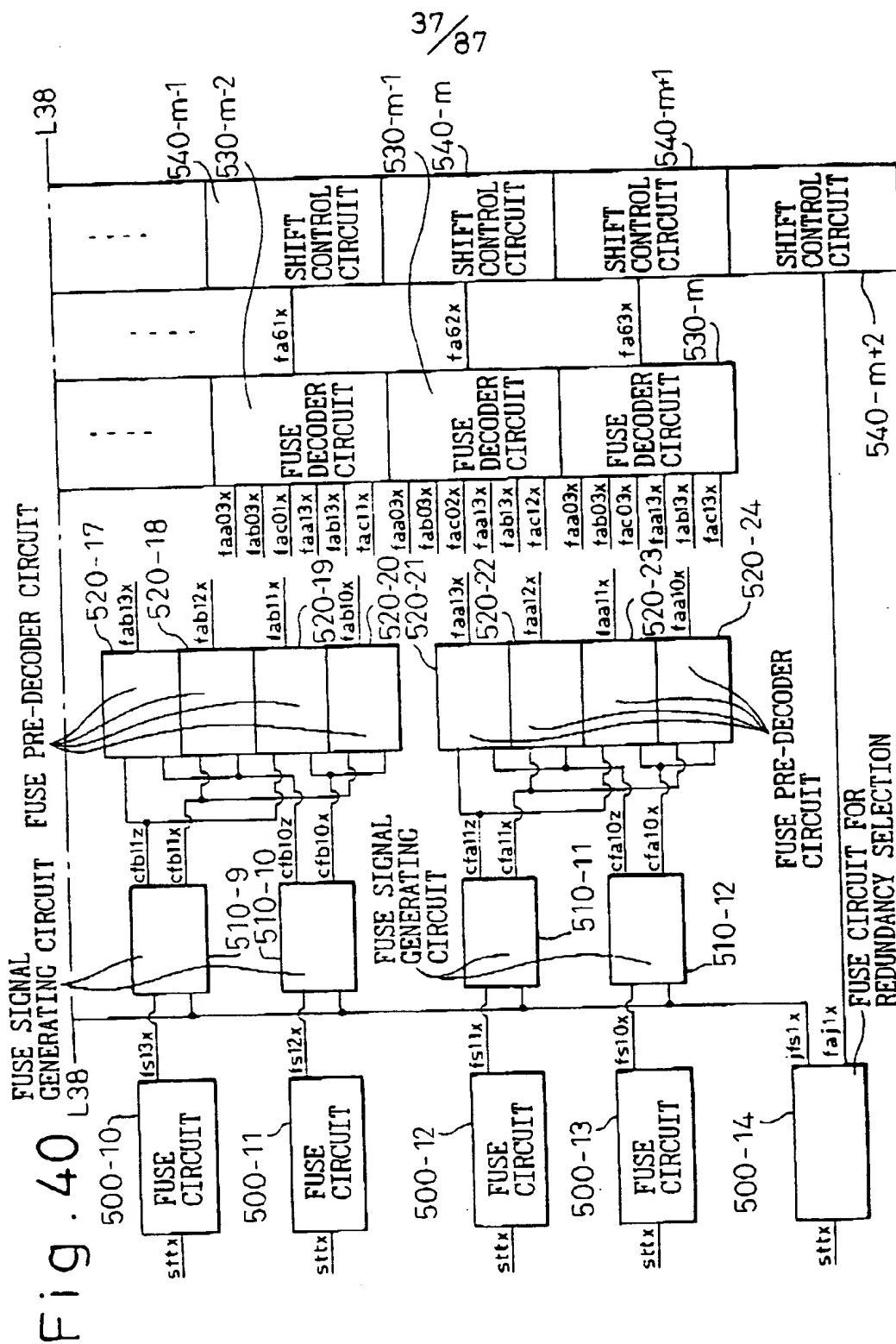


09369767-072209

Fig. 39.



66220" 2946560



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Fig.41

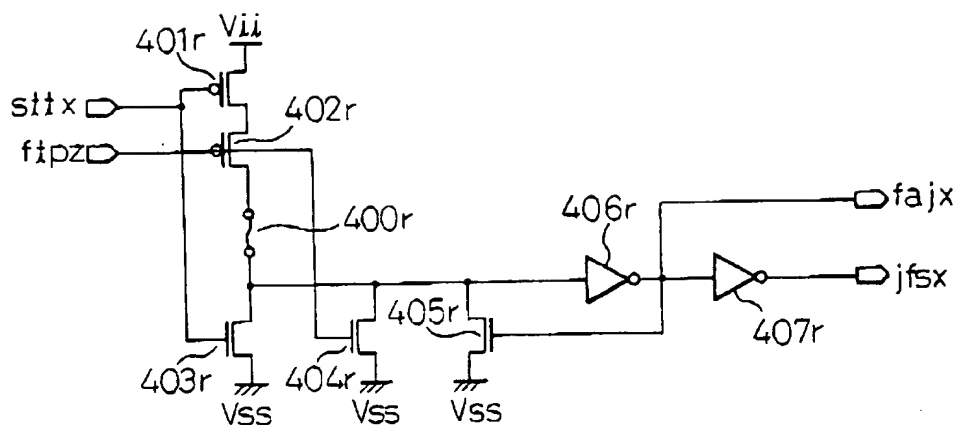
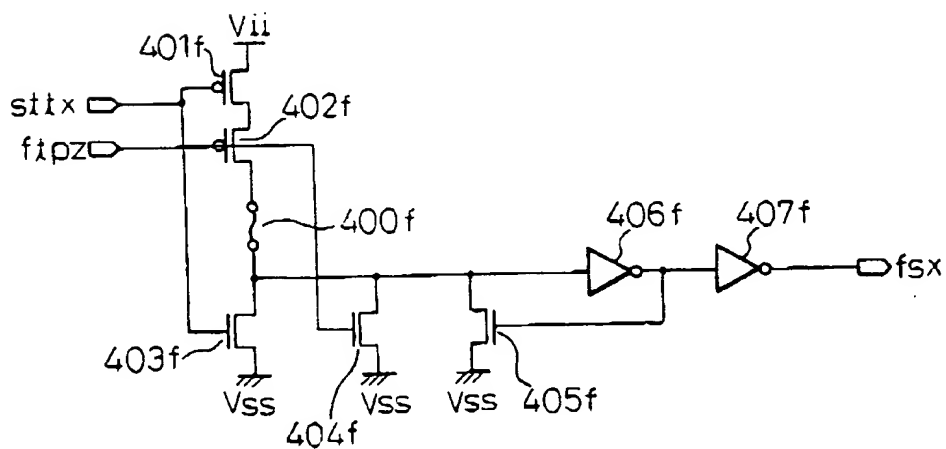


Fig.42



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Fig. 45

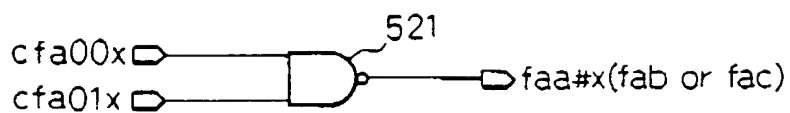
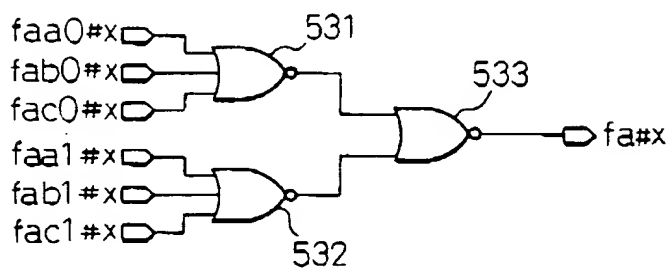


Fig. 46

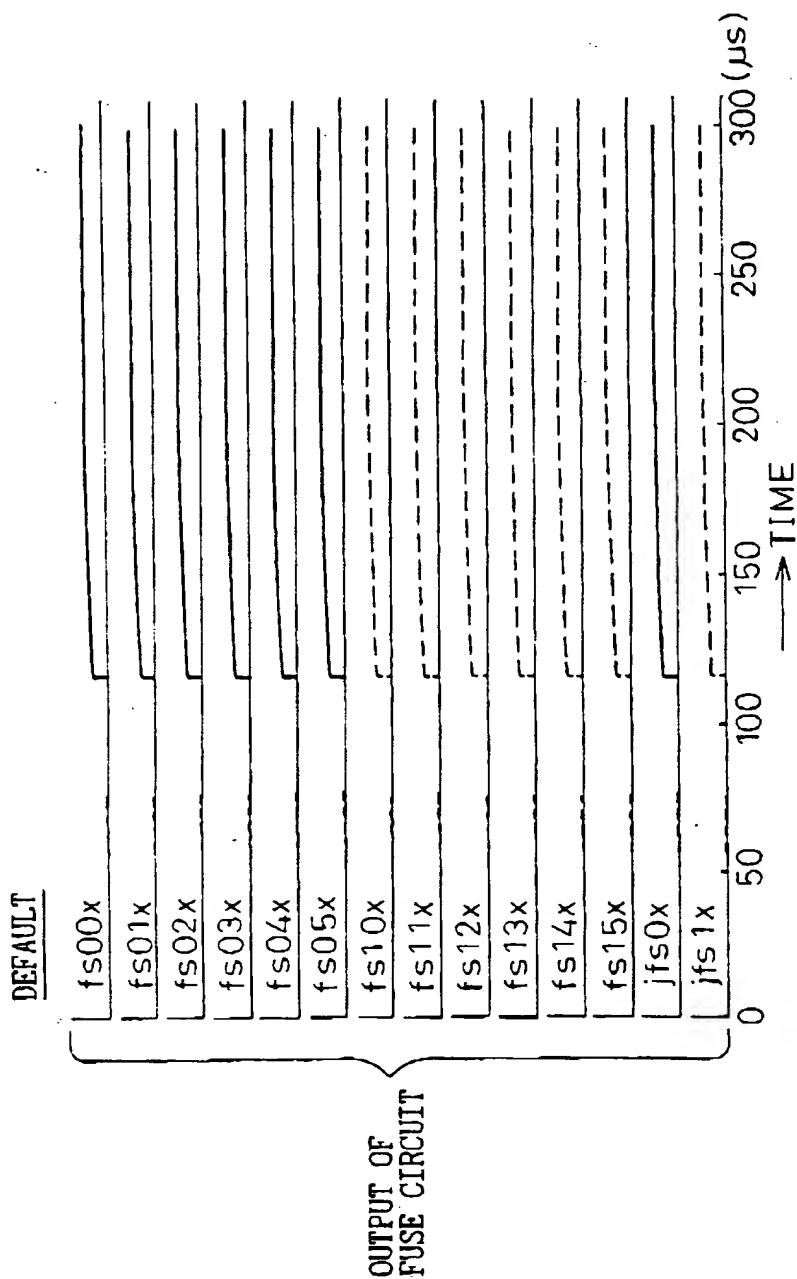


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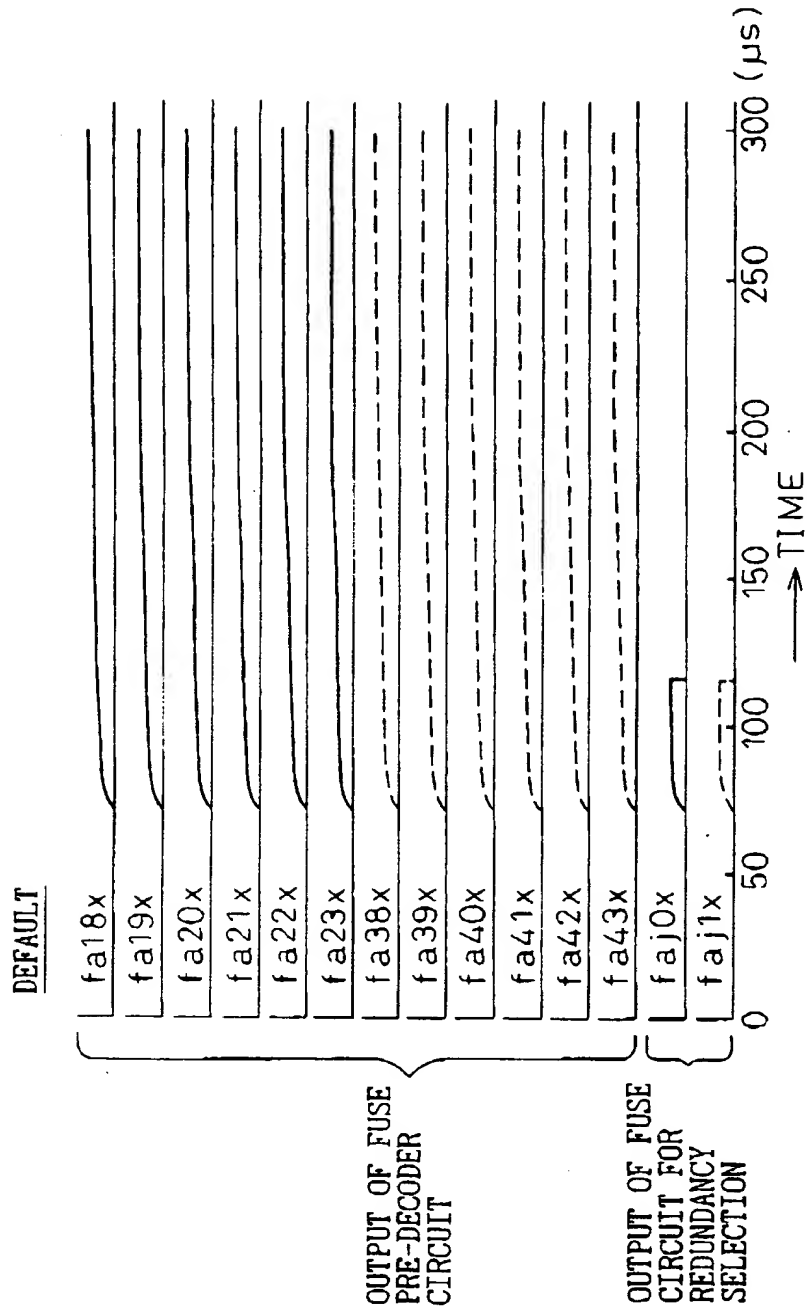
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Fig. 47



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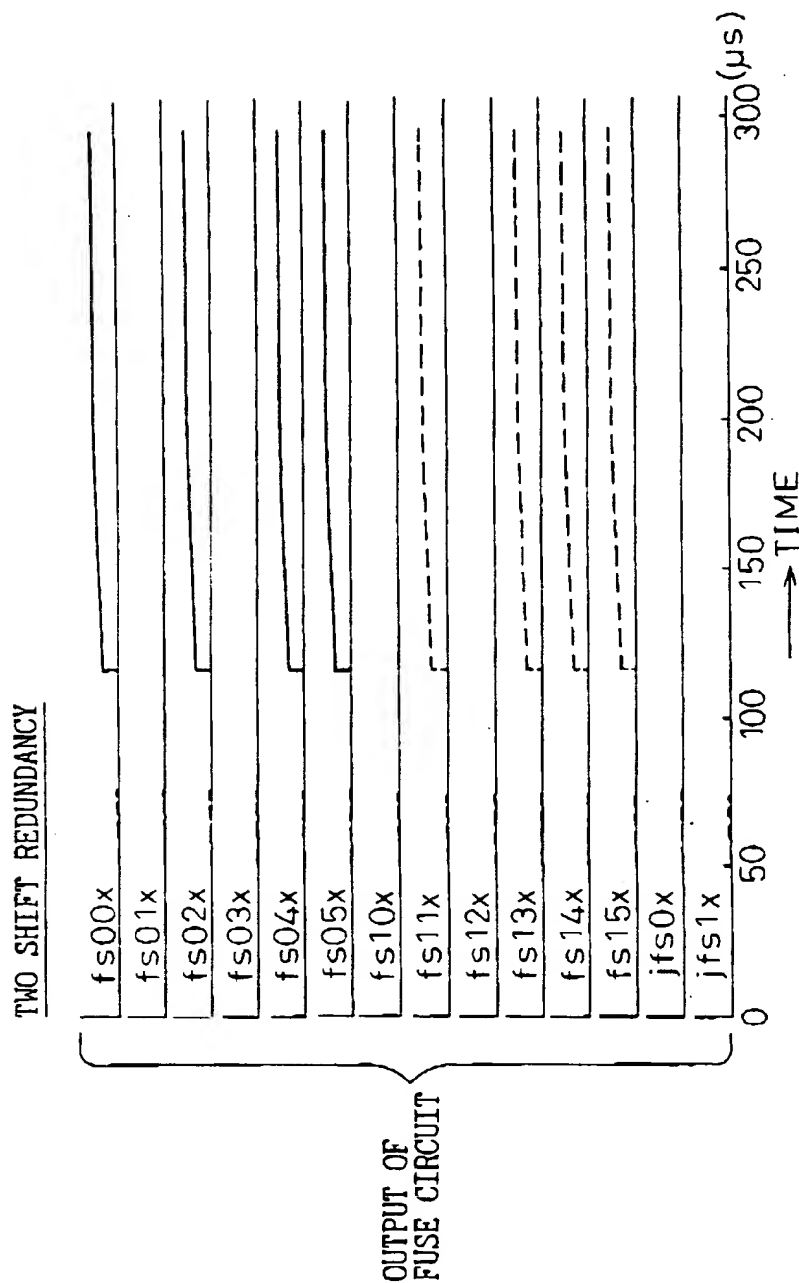
Fig. 48



652210" 49465E60

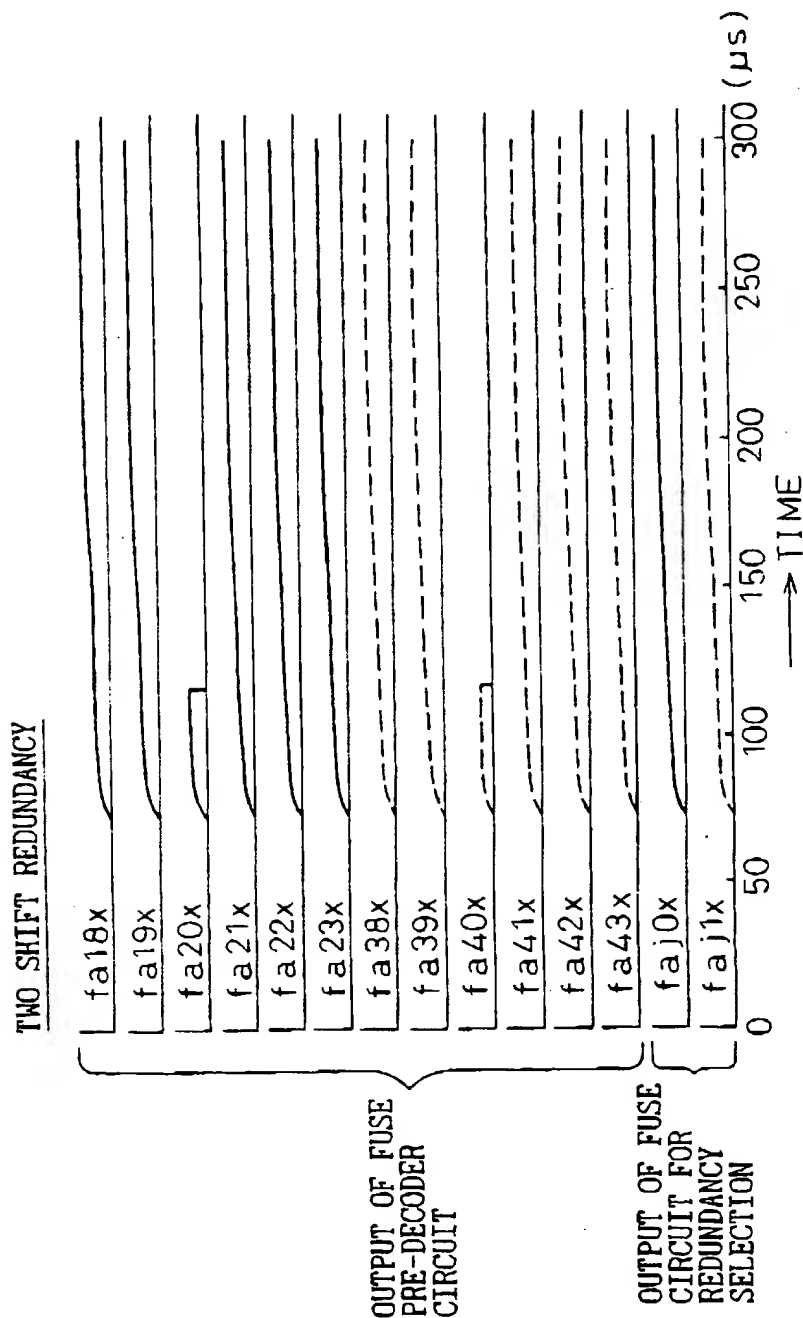
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Fig. 49



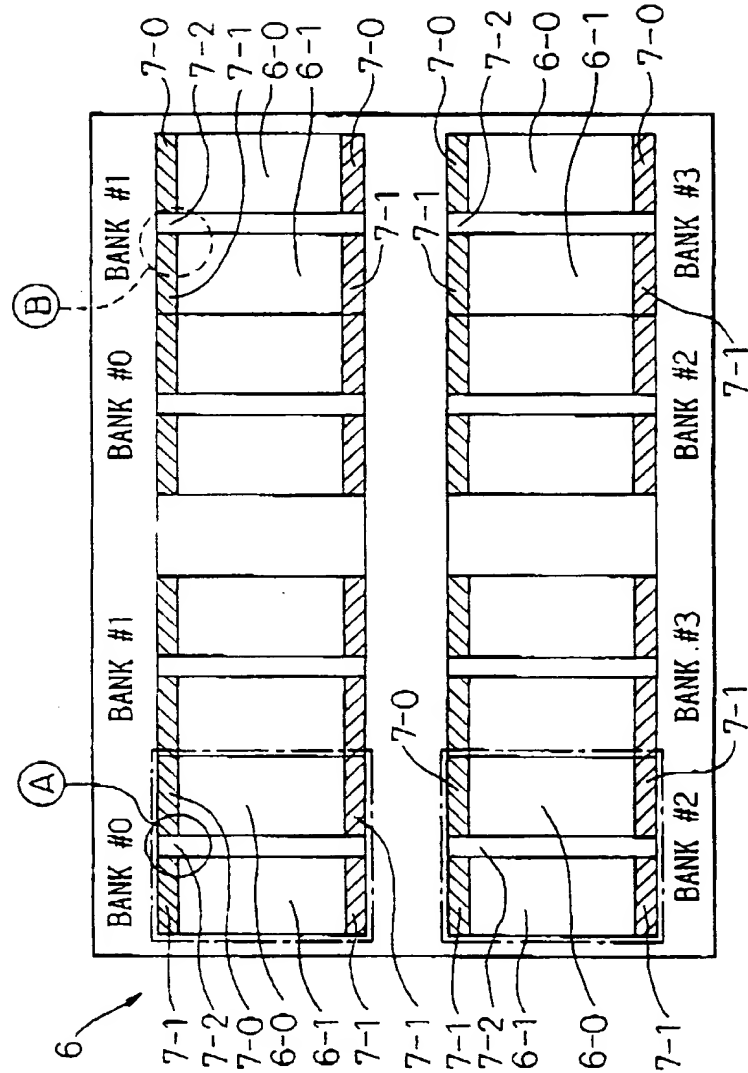
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Fig. 50



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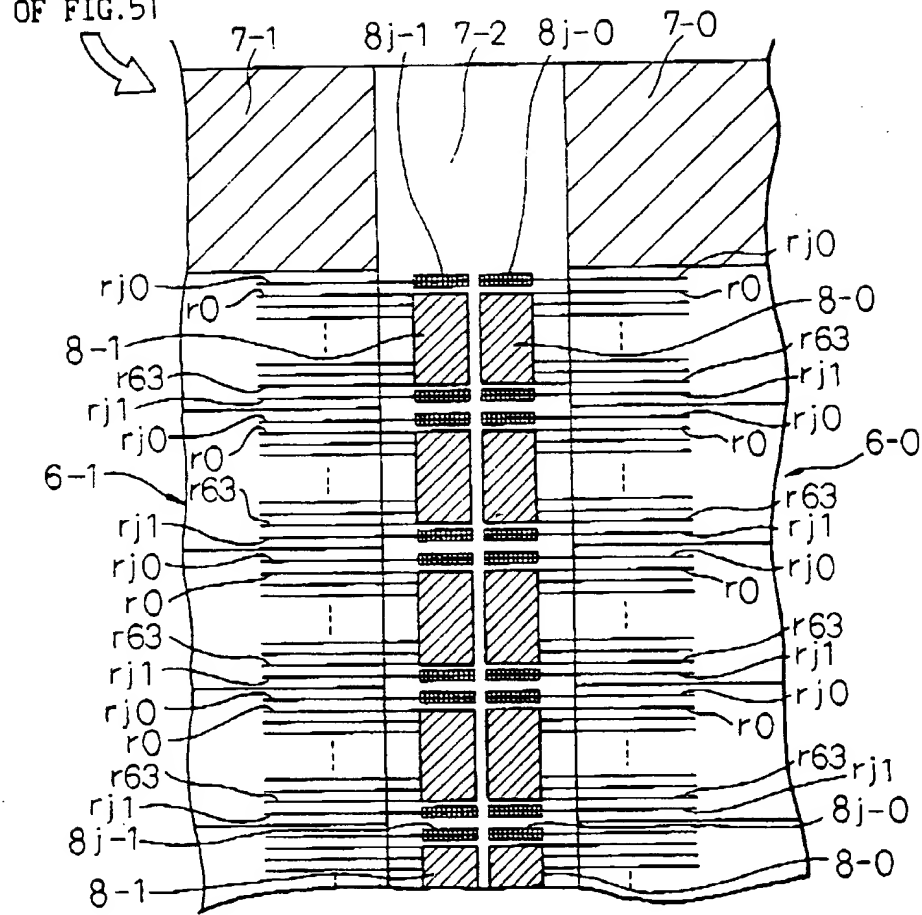
Fig. 51



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Fig. 52

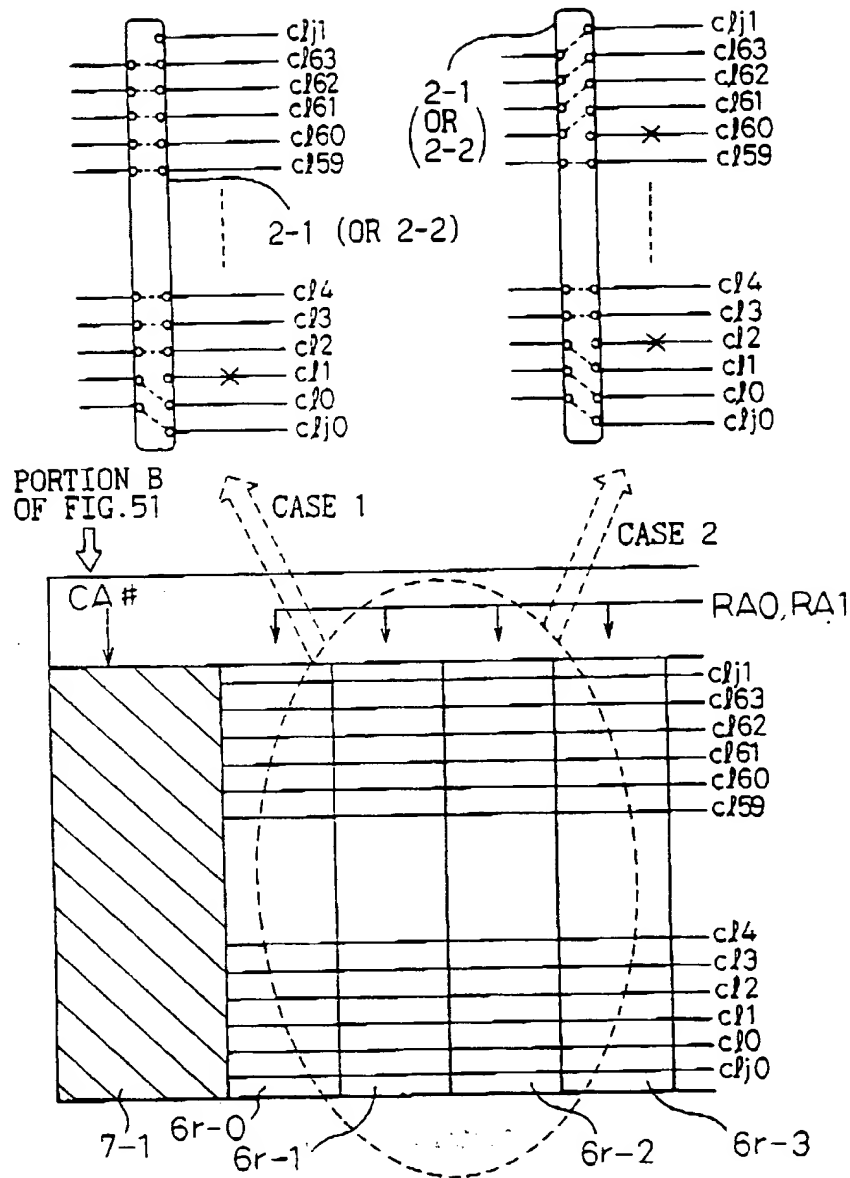
PORION A  
OF FIG. 51



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Fig.53

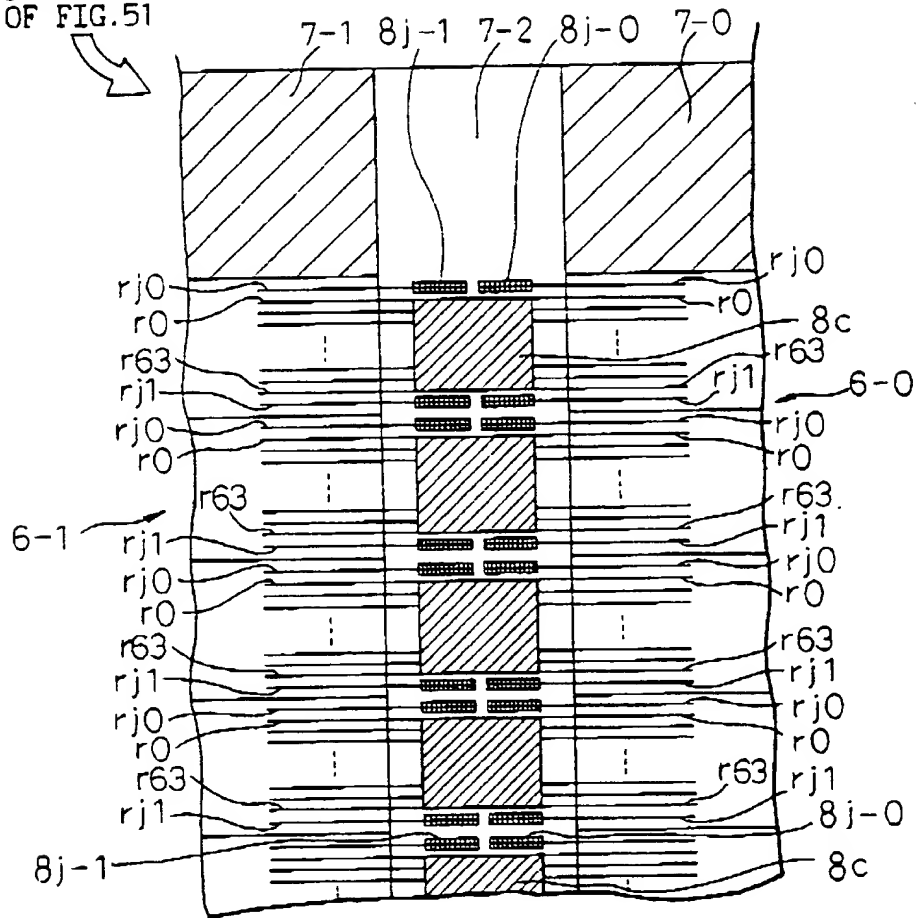


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Fig. 54

PORTION A  
OF FIG. 51

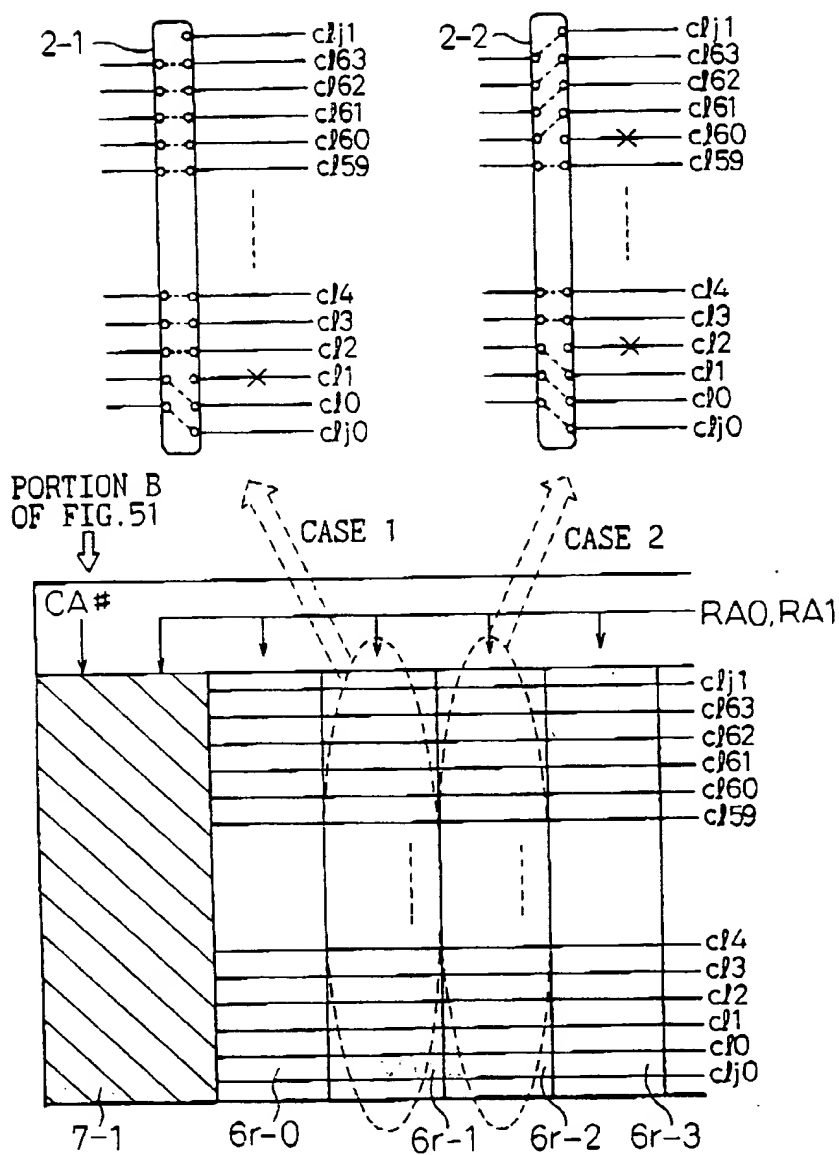


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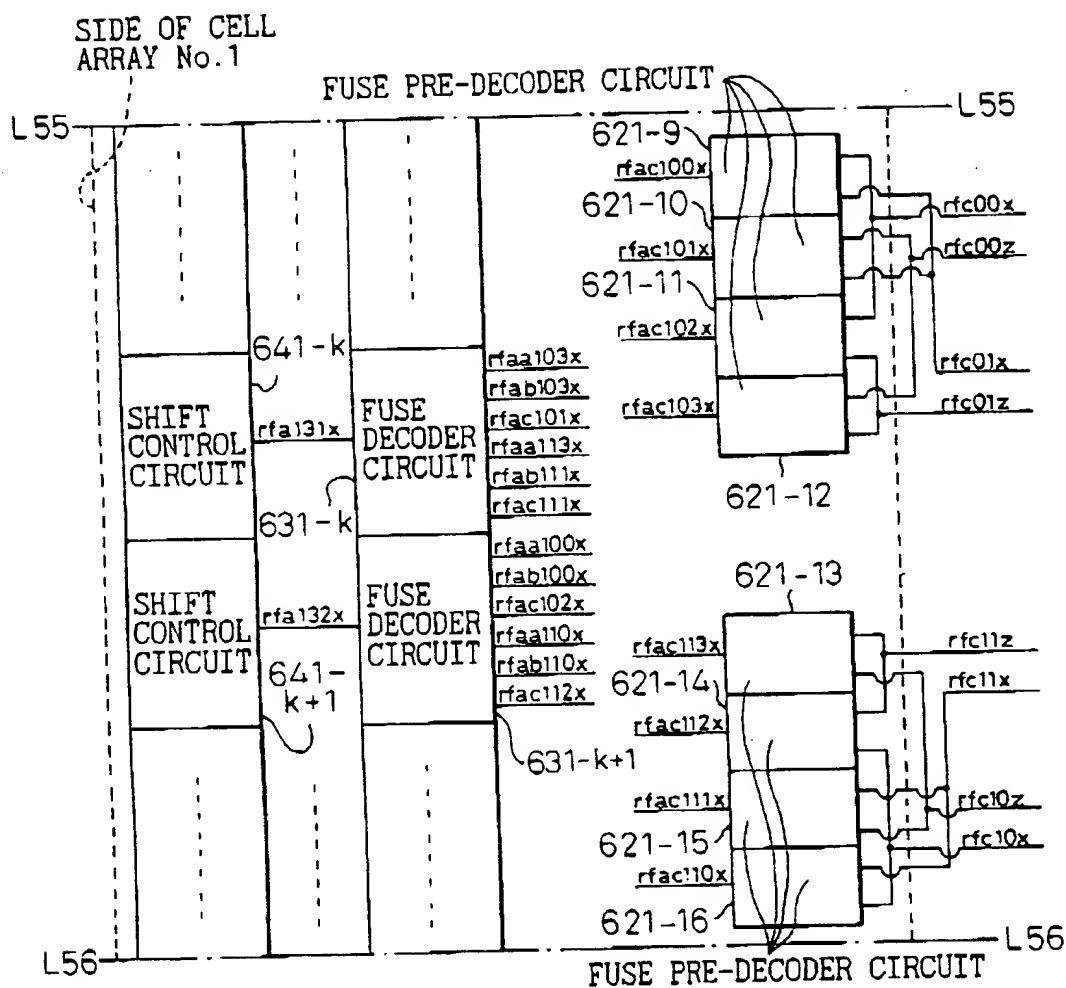
Fig. 55





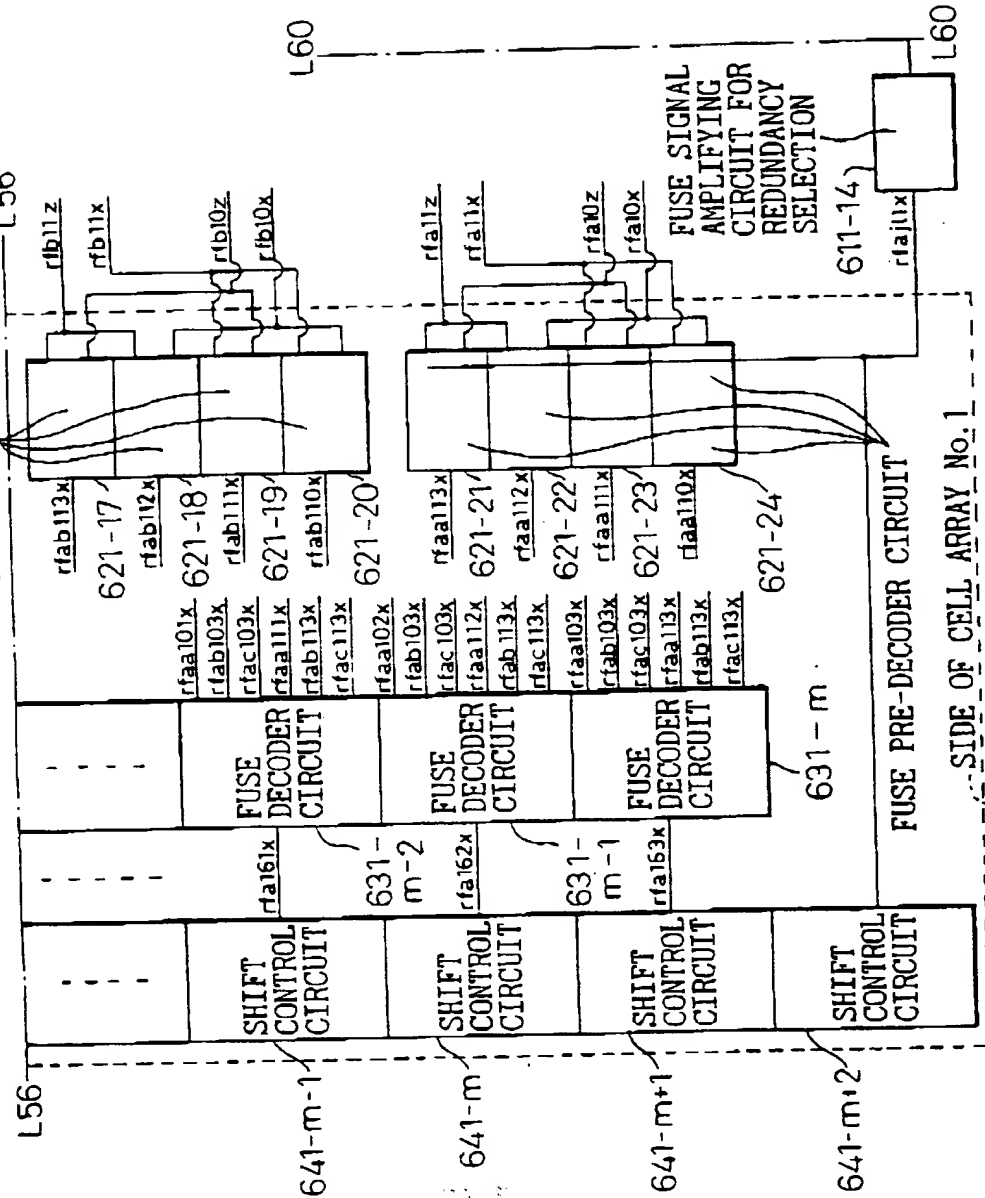
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Fig. 57



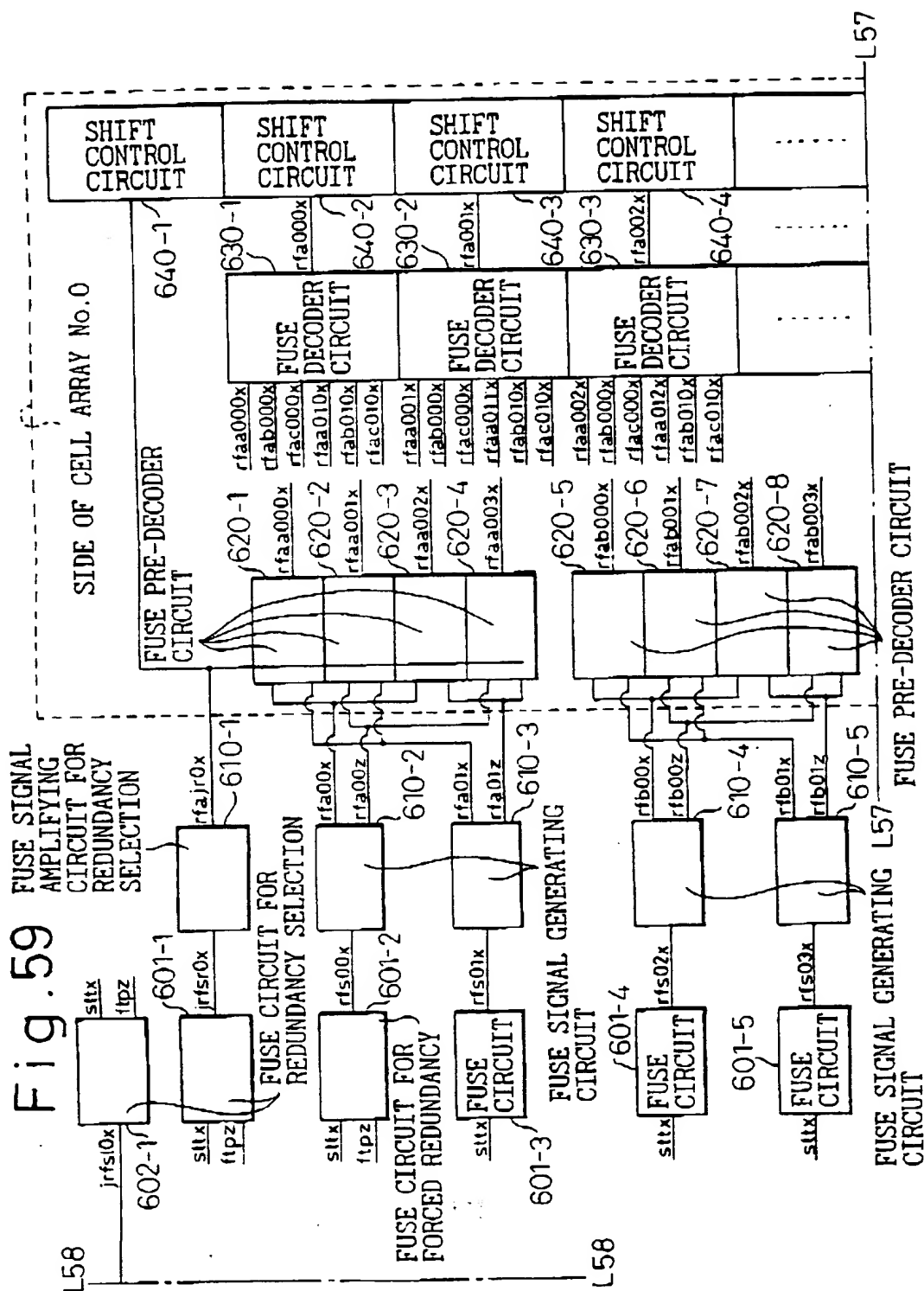
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Fig. 58 FUSE PRE-DECODER CIRCUIT

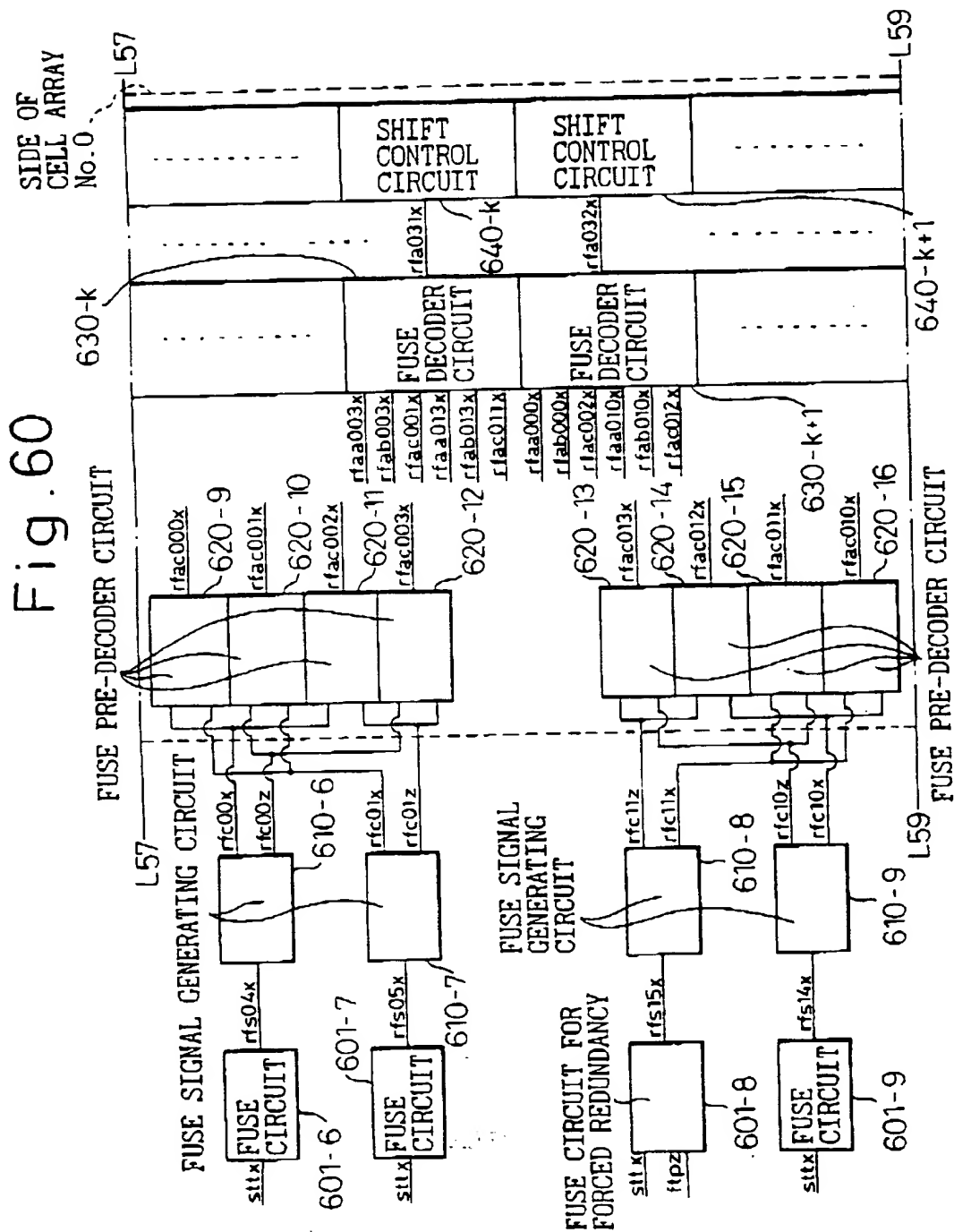


**000000000000**

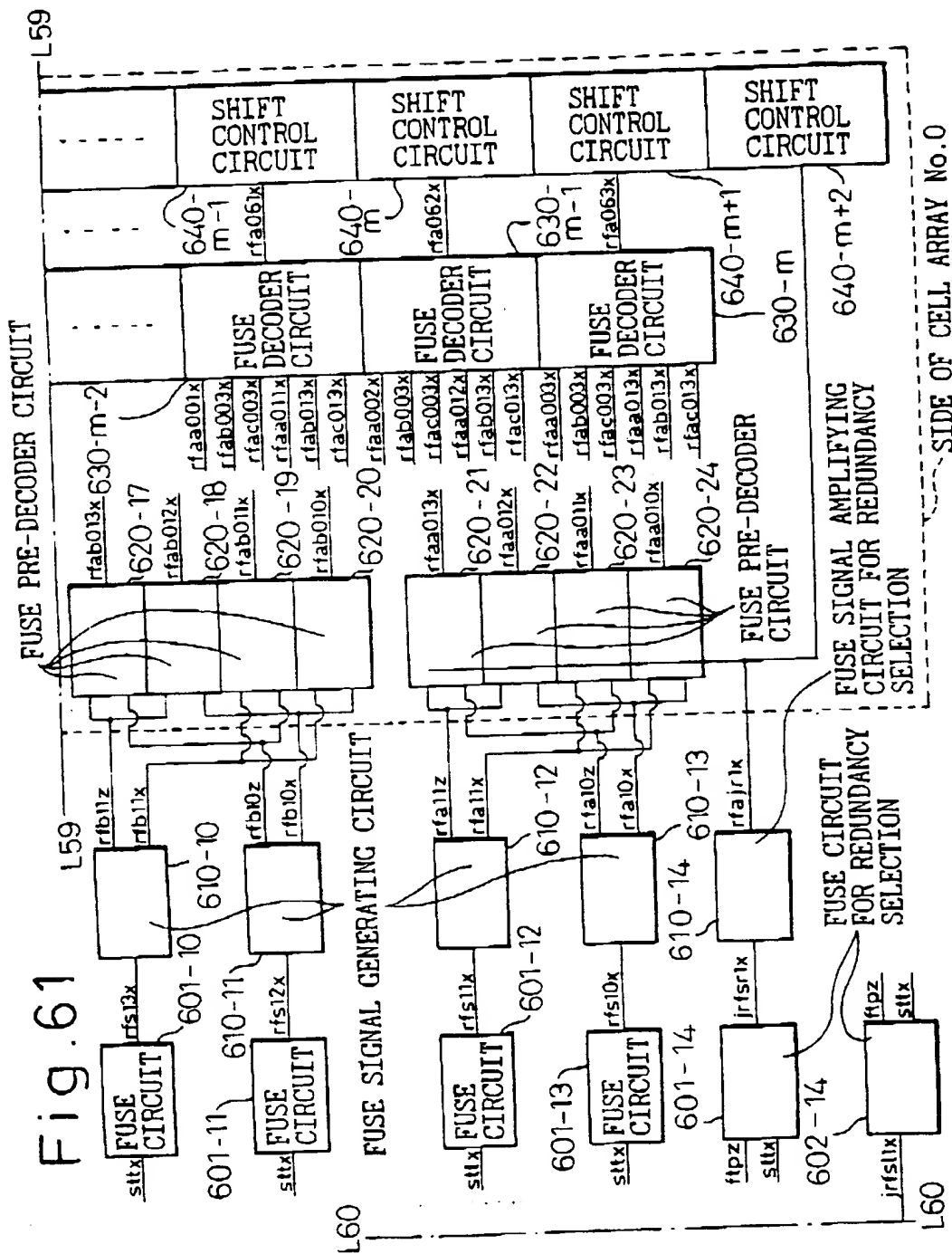
Fig. 59.



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Fig. 62

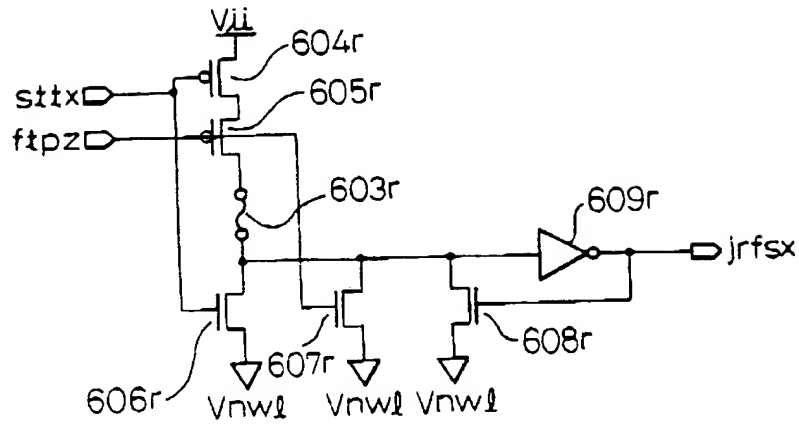
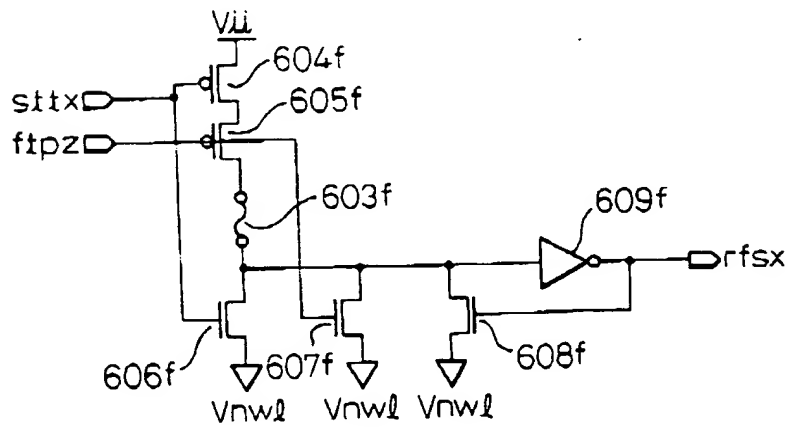


Fig. 63



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Fig. 64

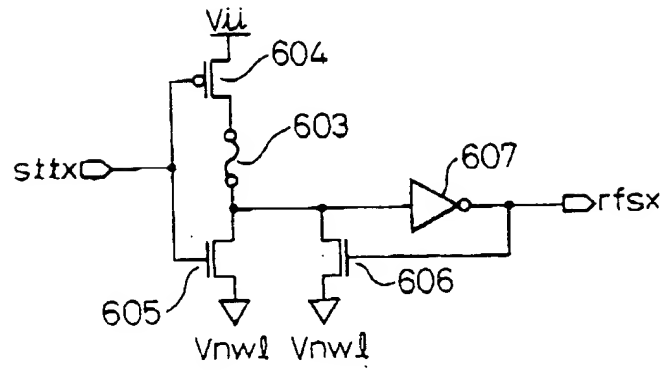


Fig. 65

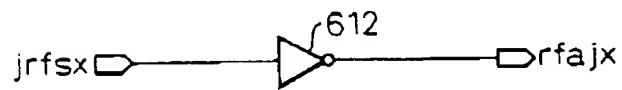
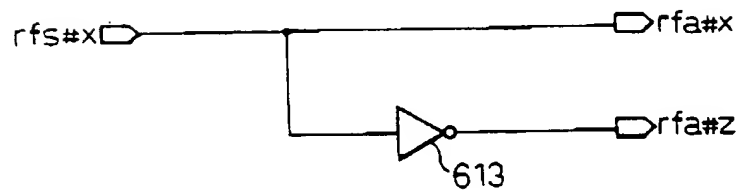


Fig. 66



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Fig. 67

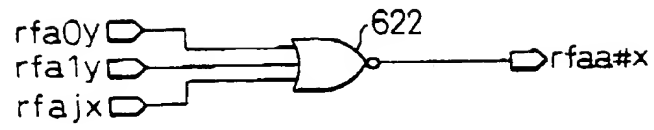


Fig. 68

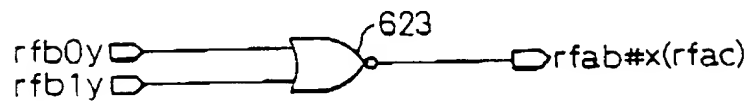
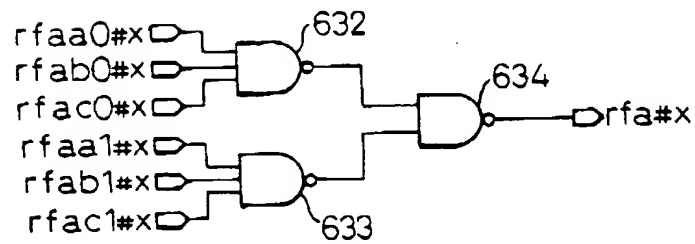


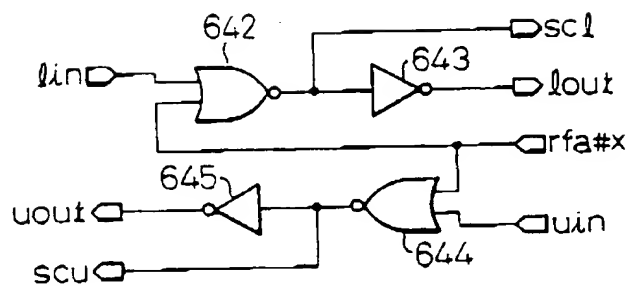
Fig. 69



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Fig.70



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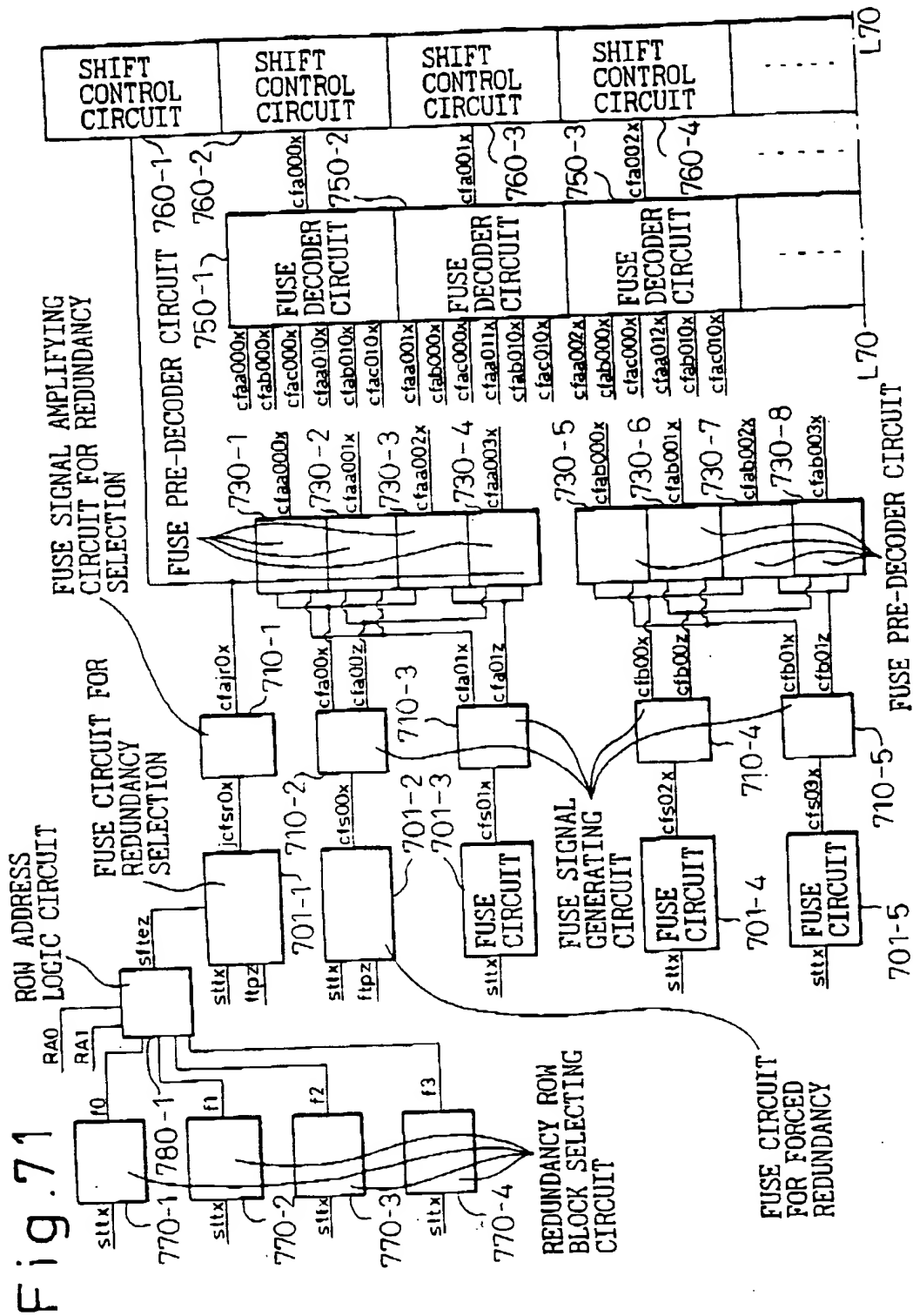


Fig. 72

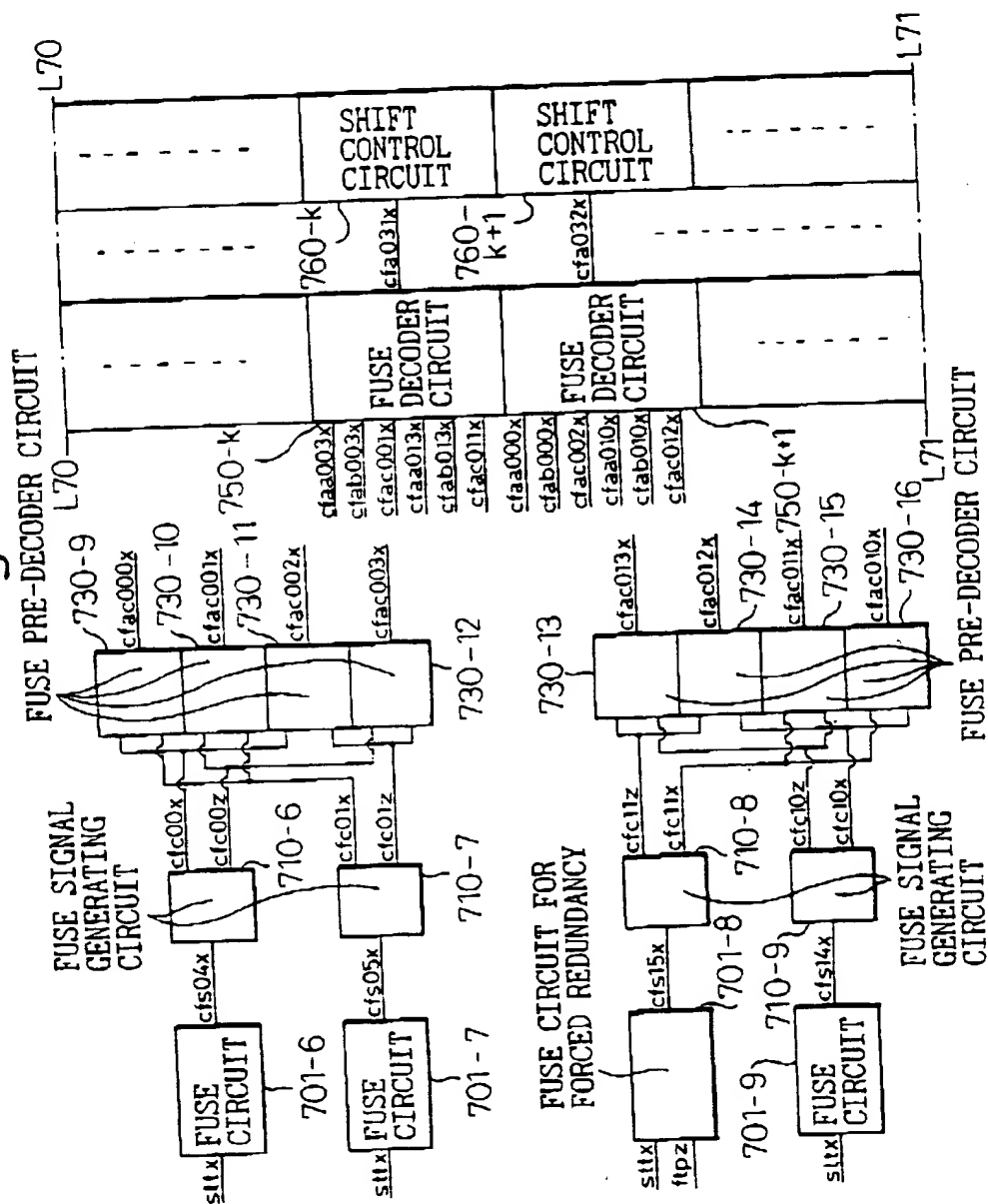
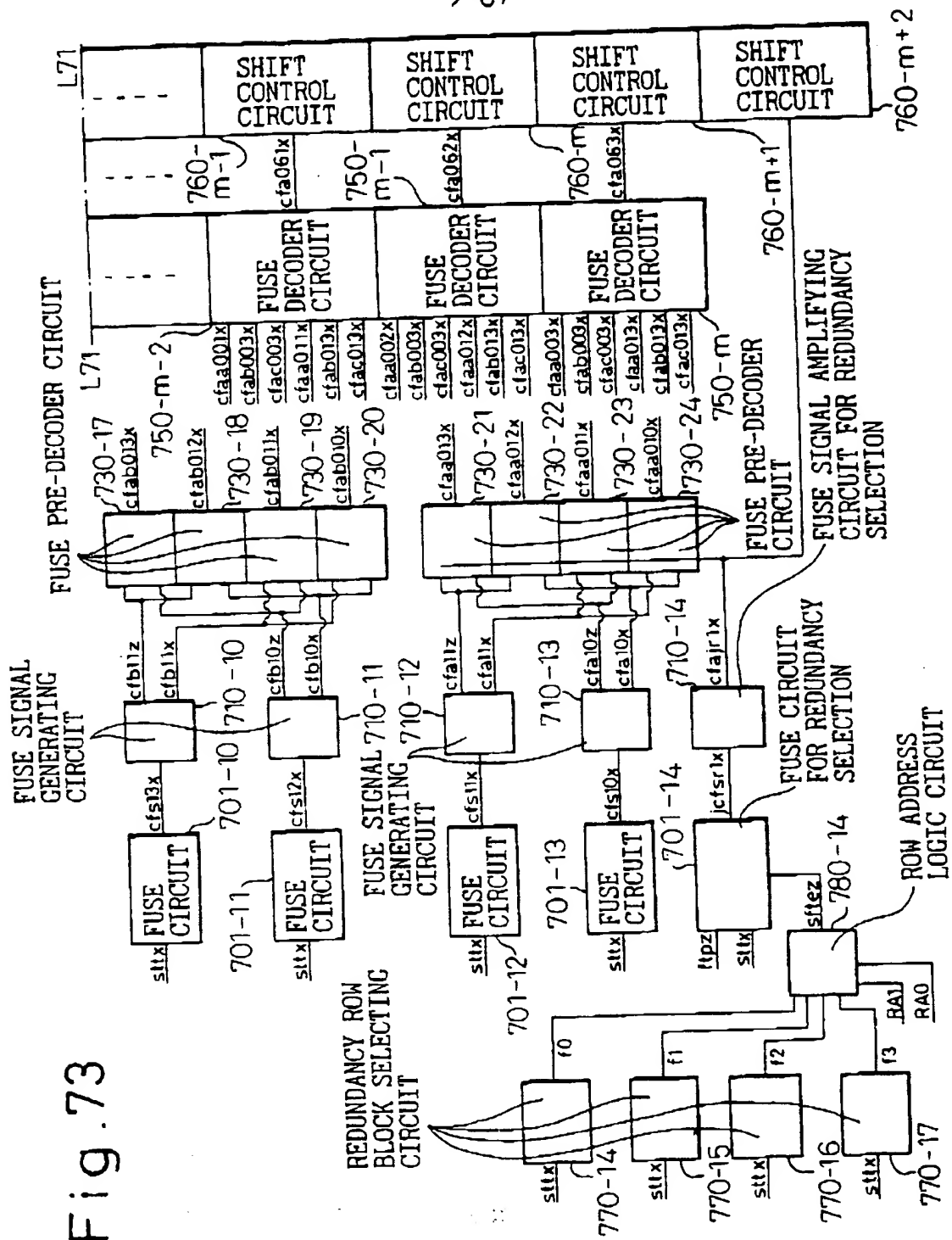


Fig. 73



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Fig.74

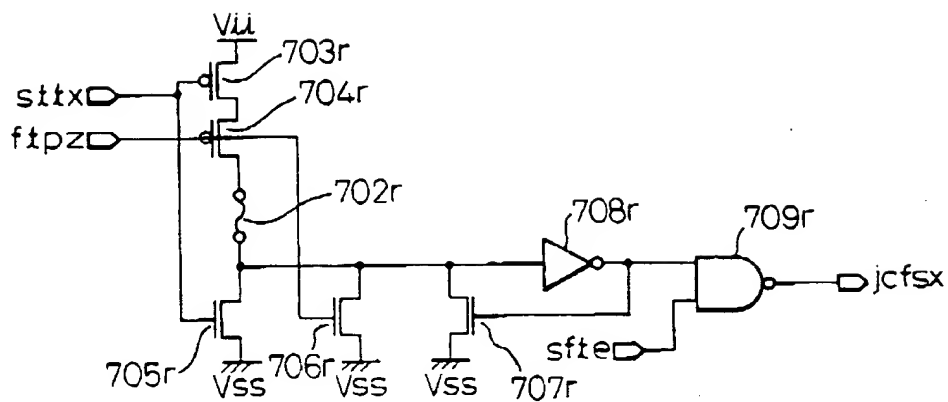
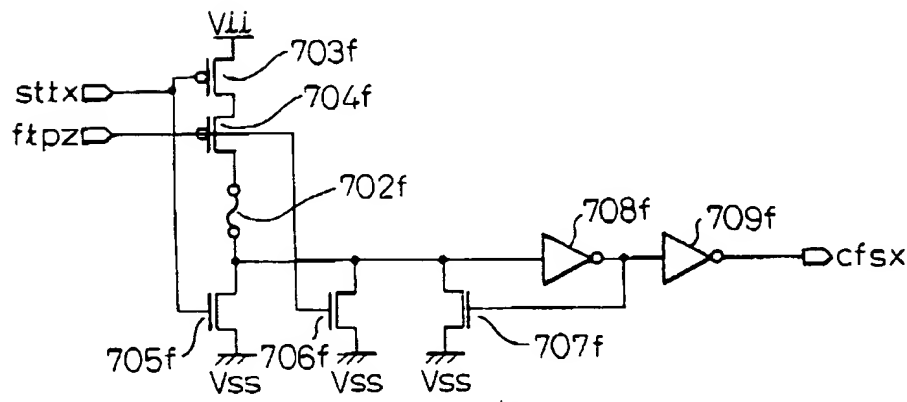


Fig.75



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Fig.76

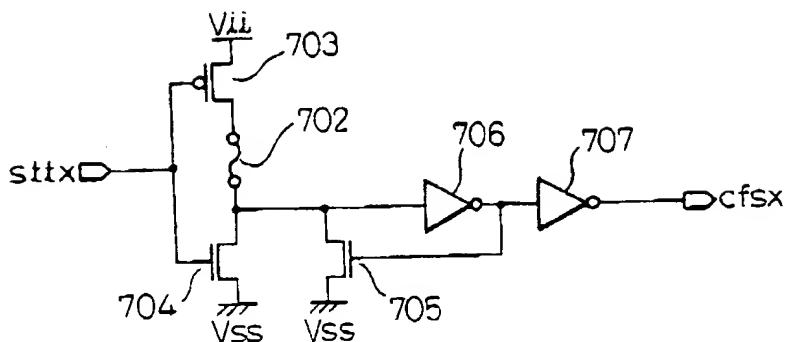


Fig.77

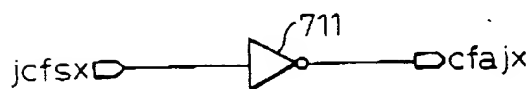
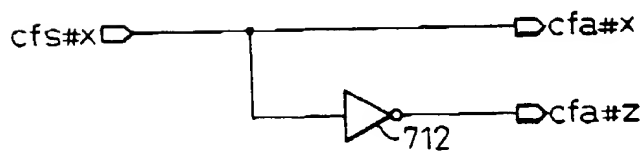


Fig.78



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Fig.79

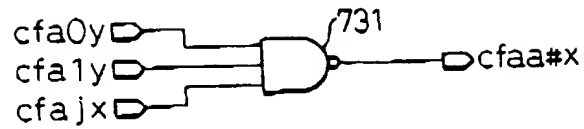


Fig.80

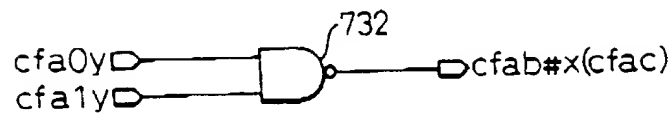
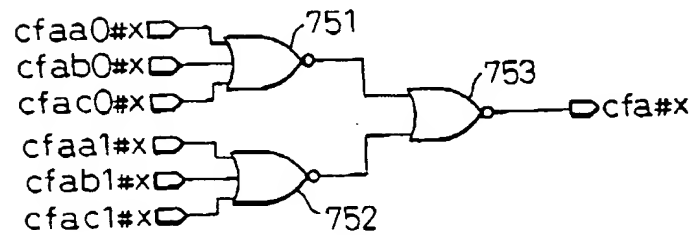


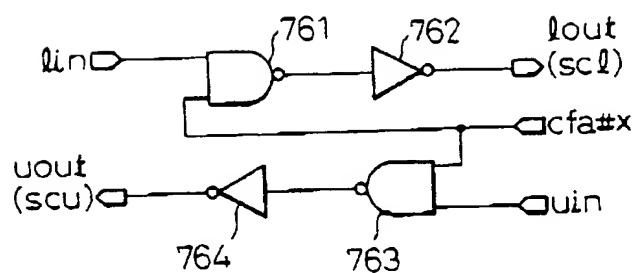
Fig.81



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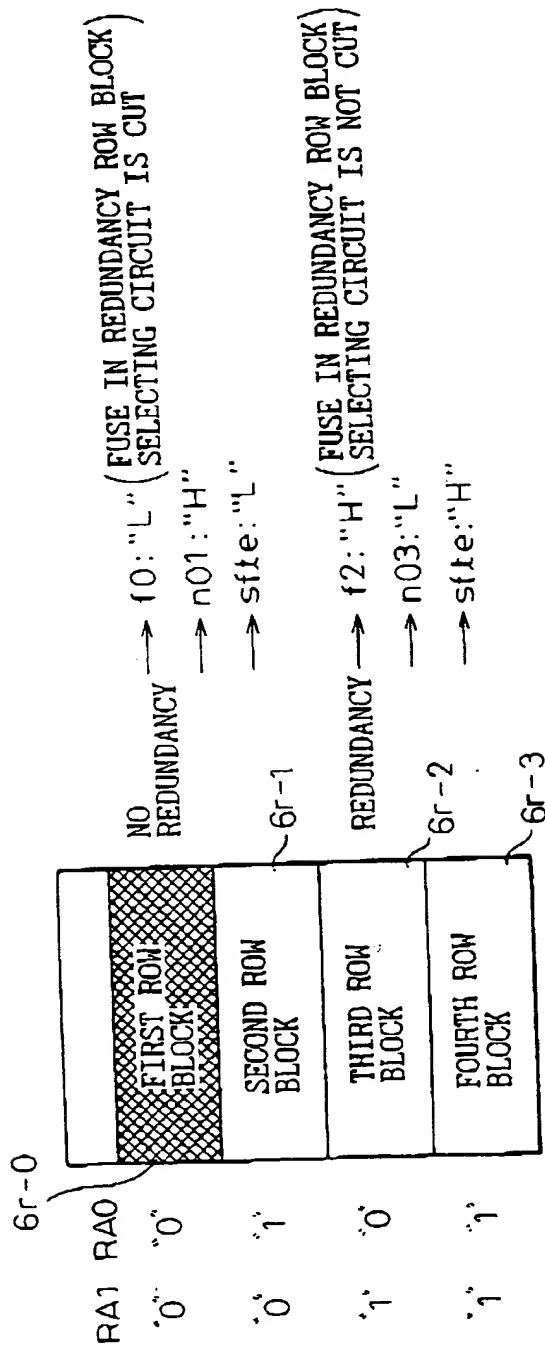
Fig. 82



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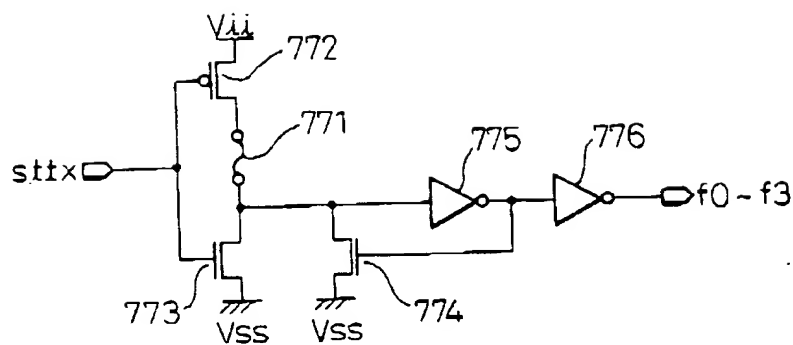
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Fig. 83



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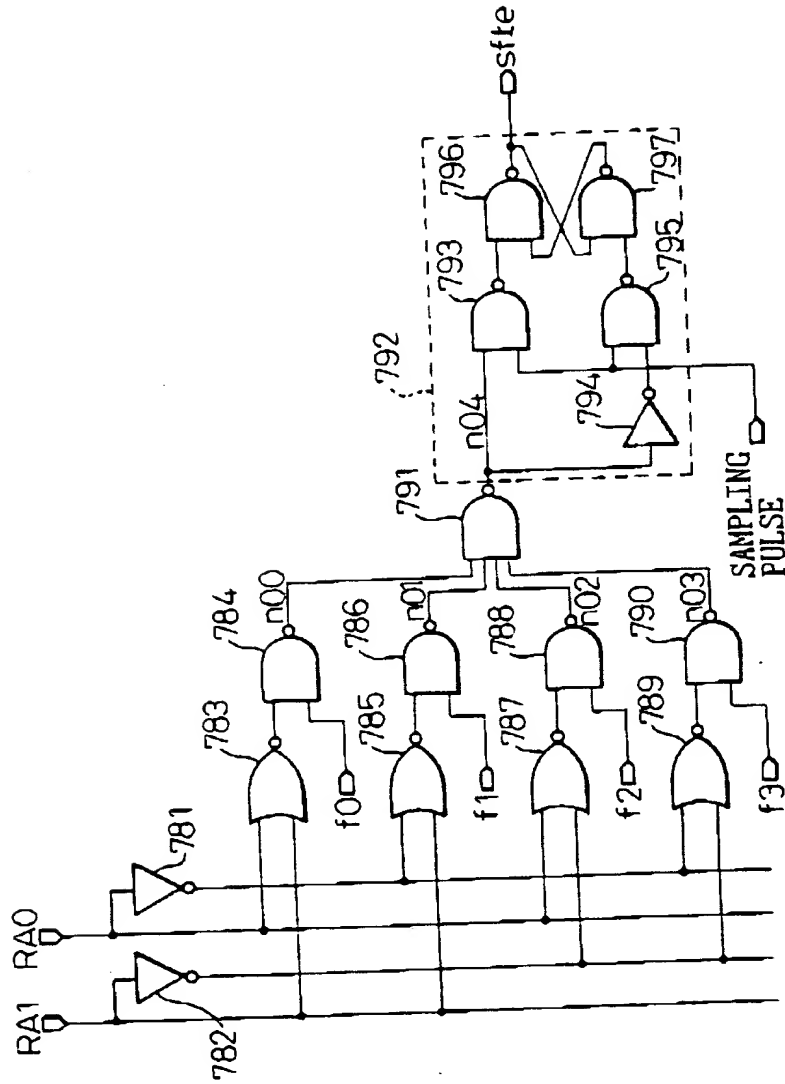
Fig.84



093976 29265E60

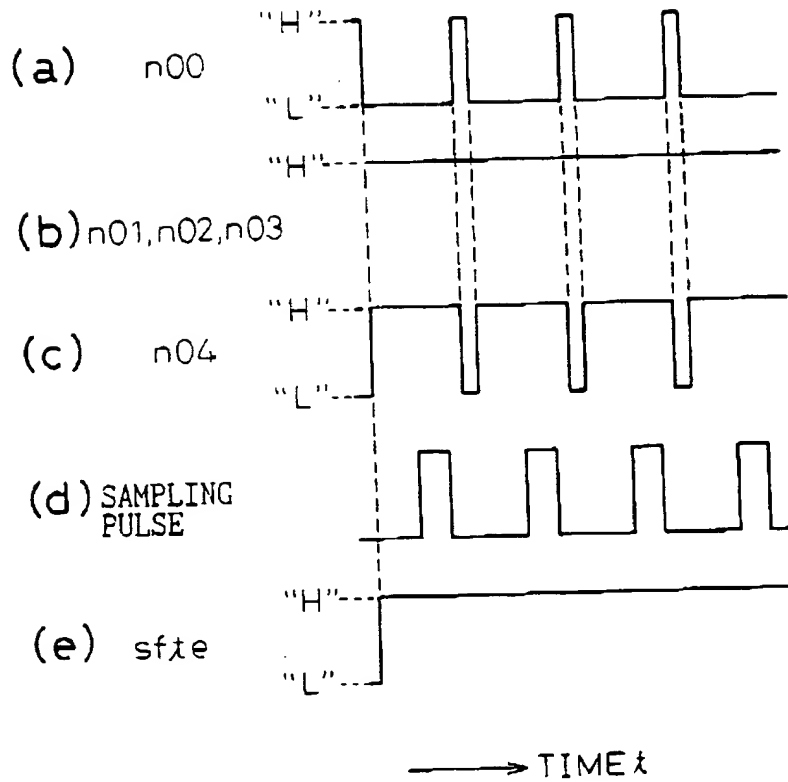
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Fig. 85



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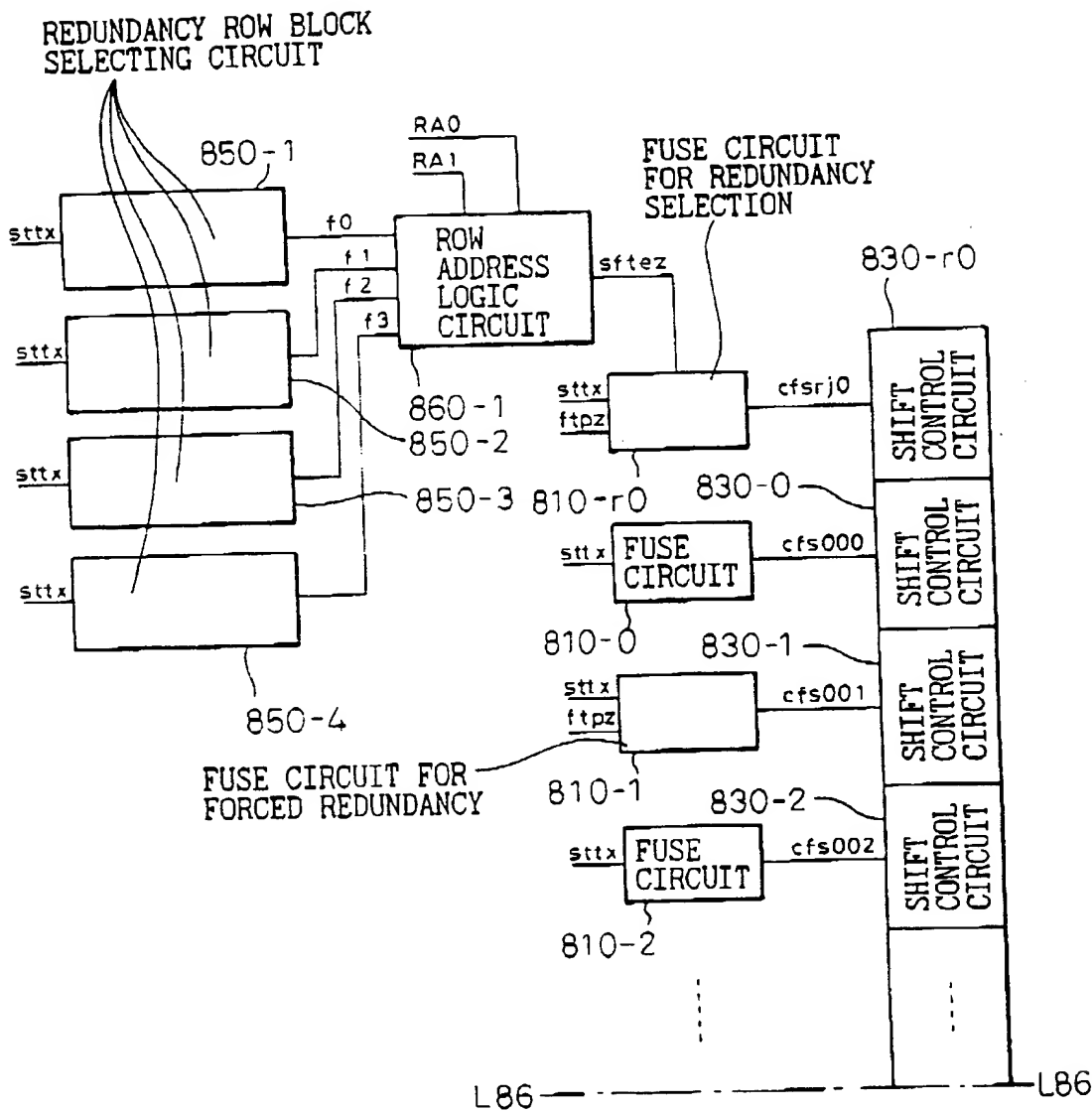
Fig.86



09359767.072299

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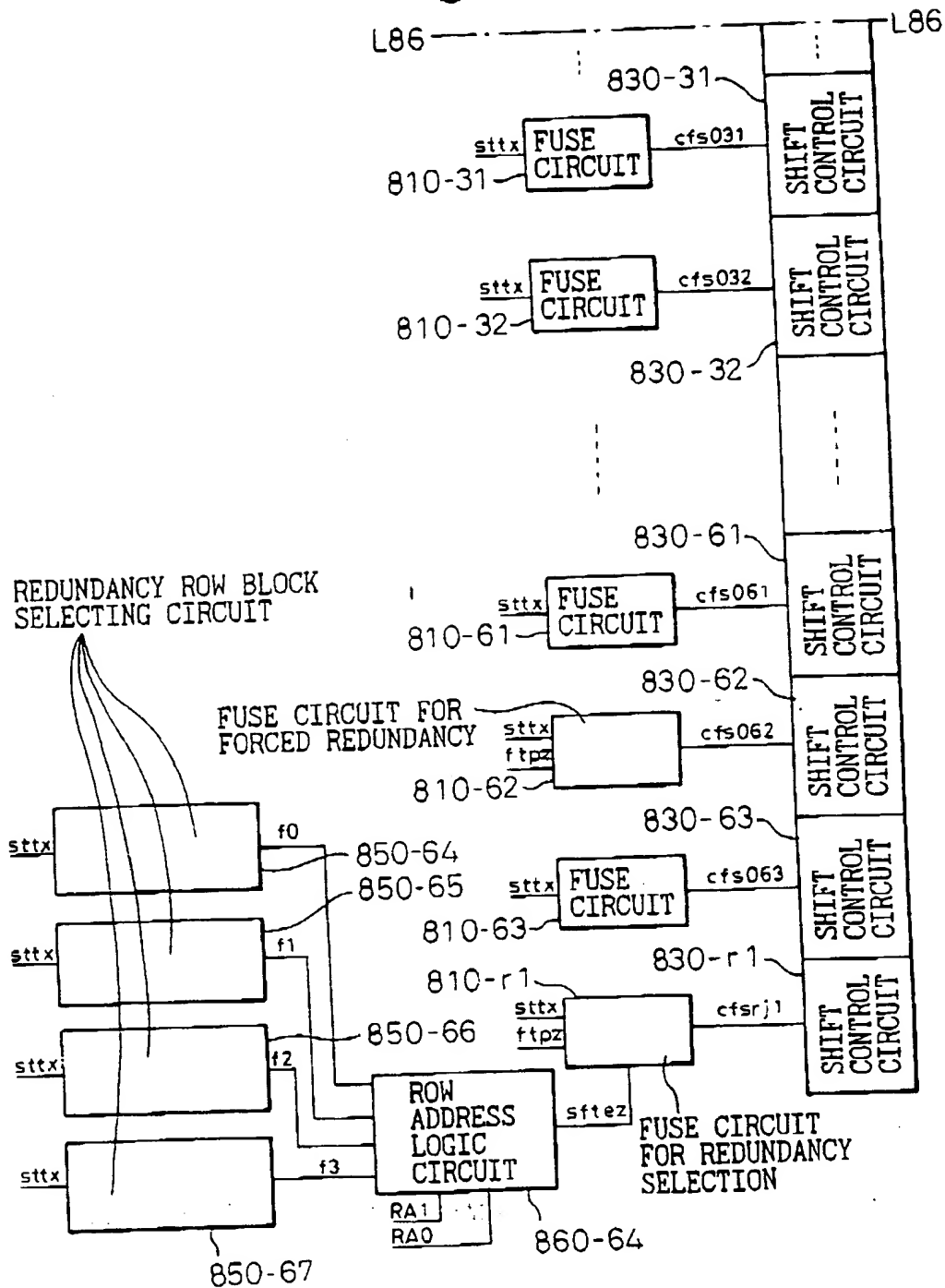
Fig.87



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Fig.88



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Fig. 89

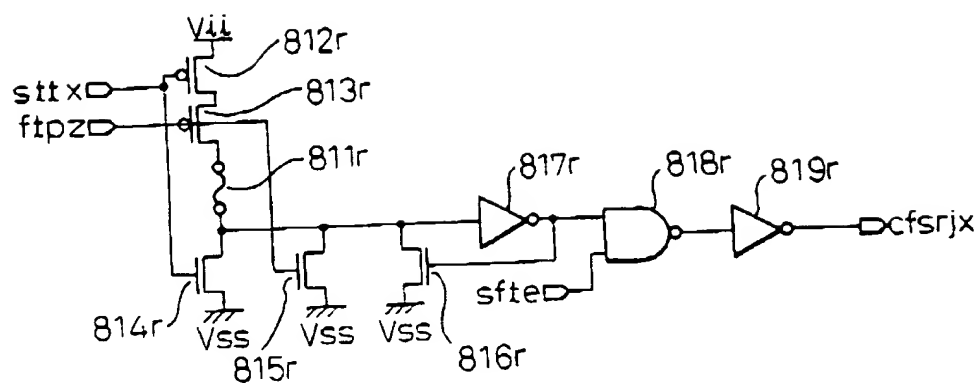
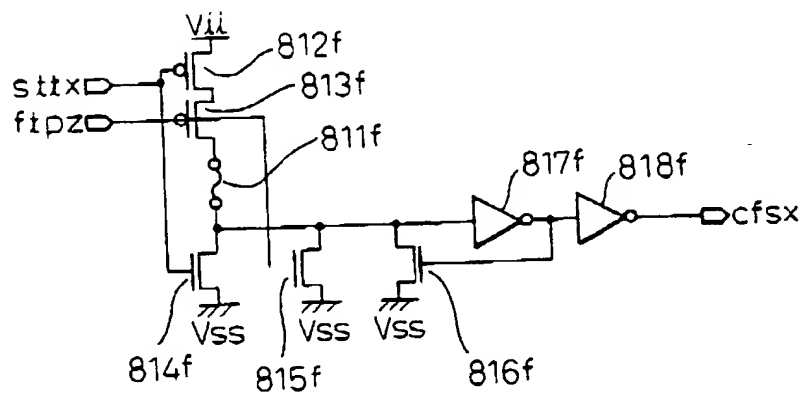


Fig. 90



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Fig. 91

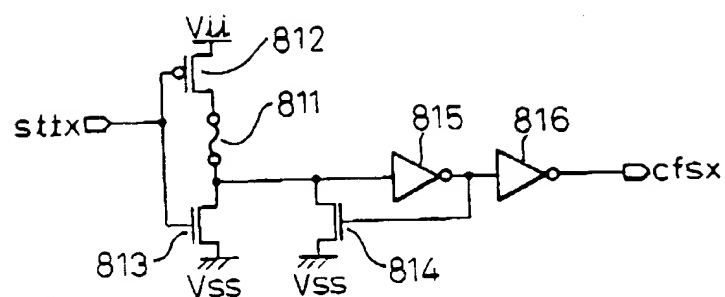
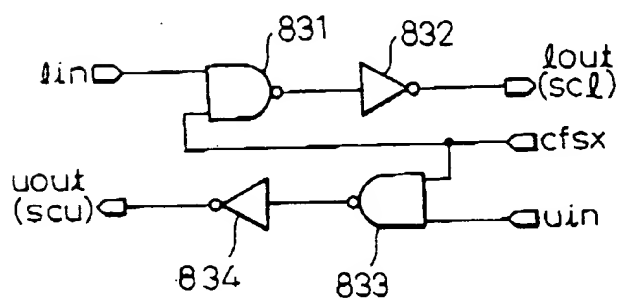
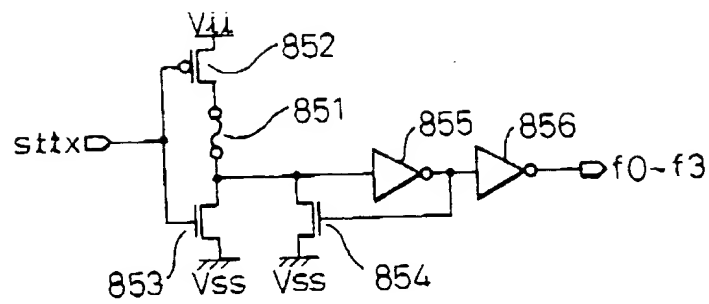


Fig. 92



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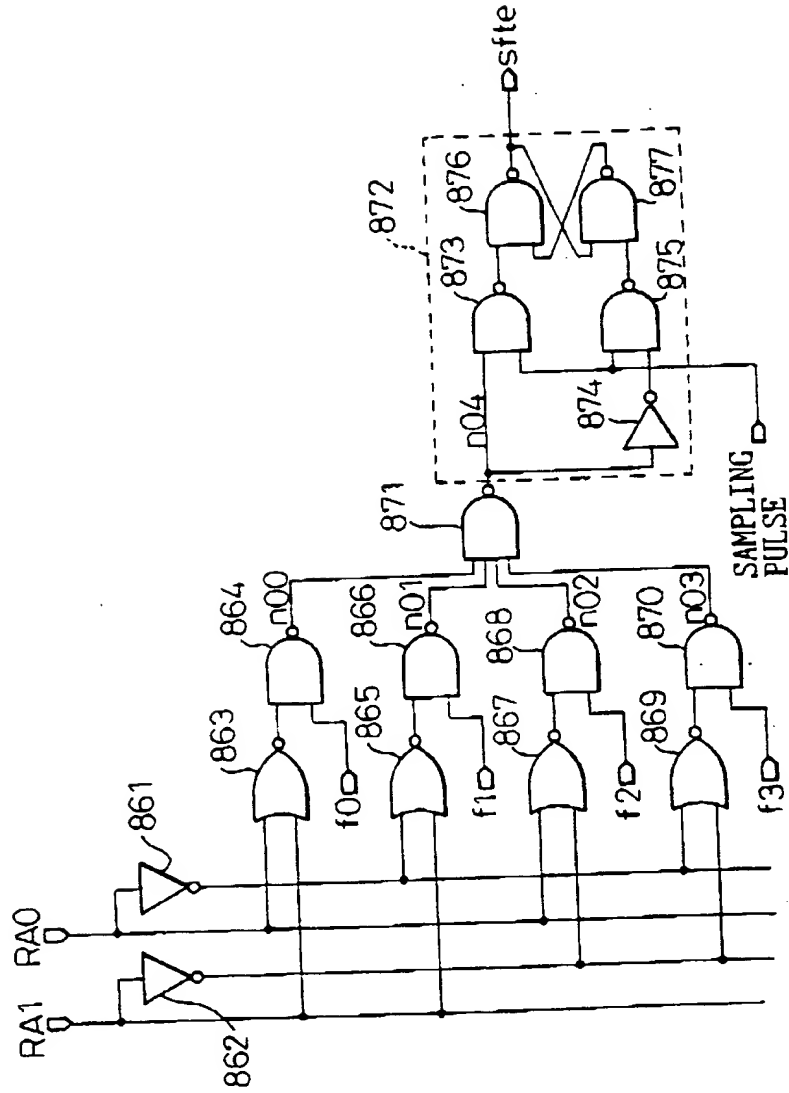
Fig. 93



09359767.07299

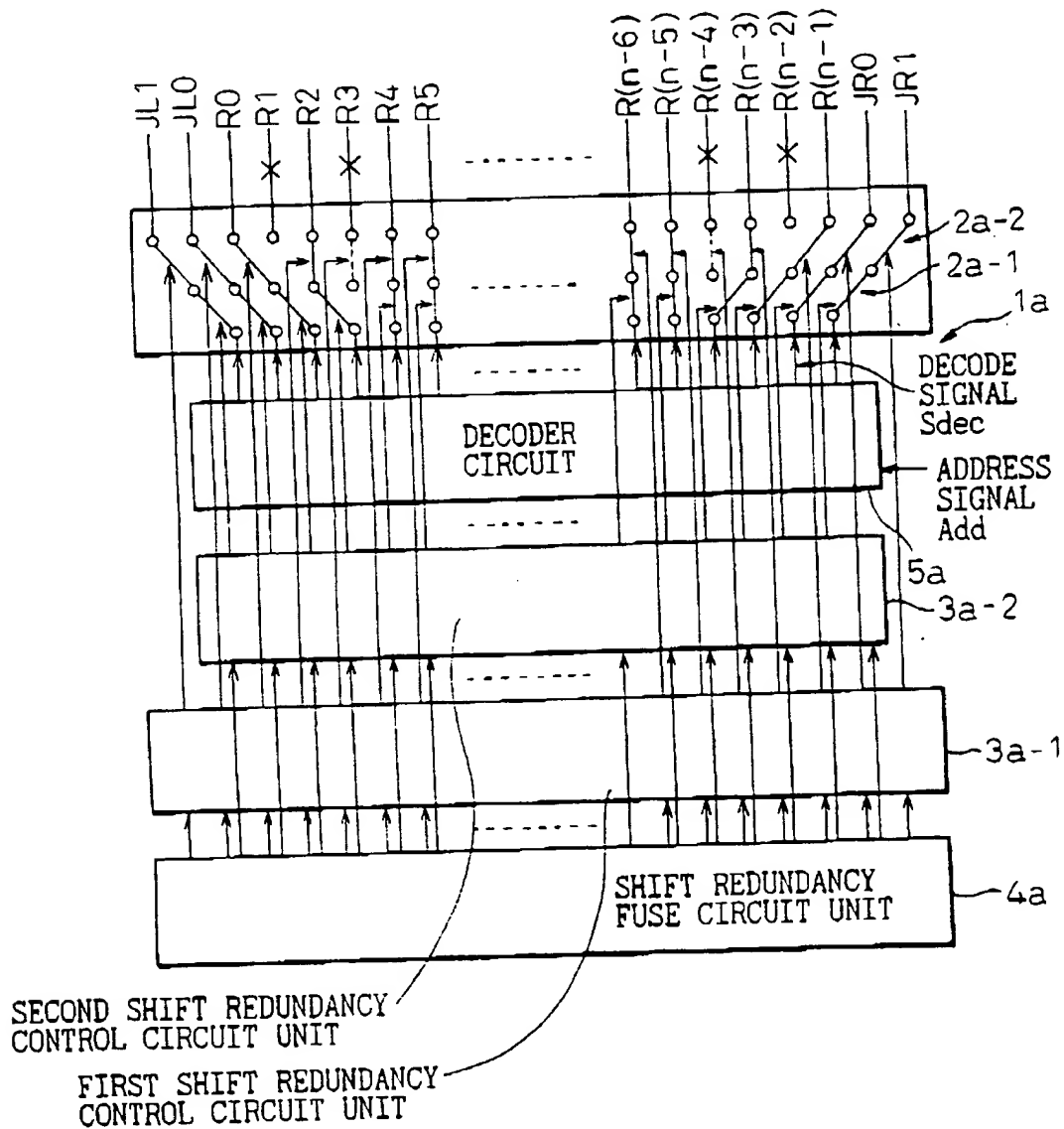
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Fig. 94



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Fig. 95



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Fig. 96

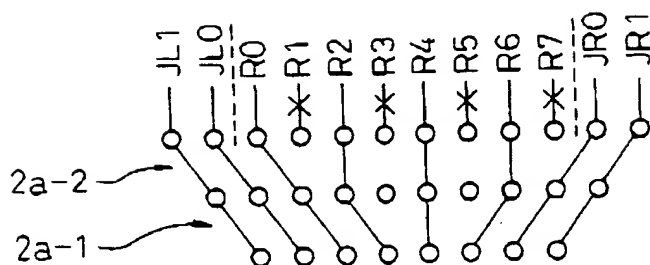


Fig. 97

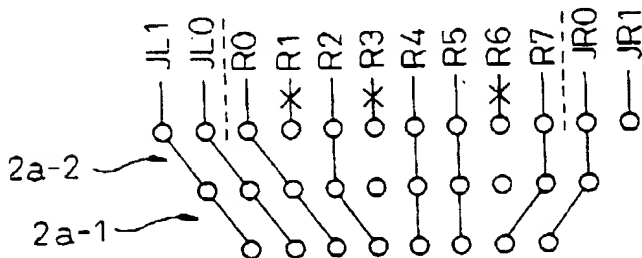
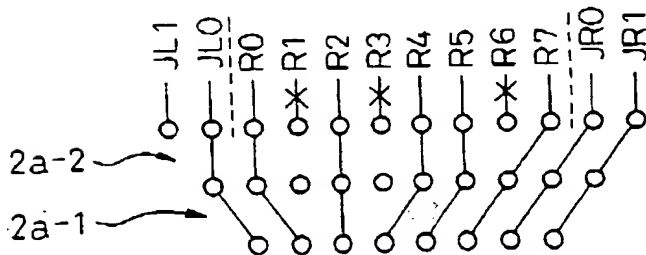


Fig. 98



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Fig. 99

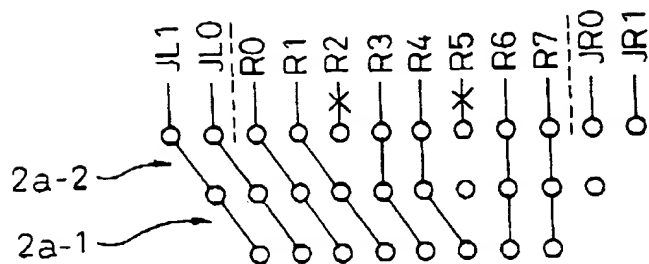


Fig. 100

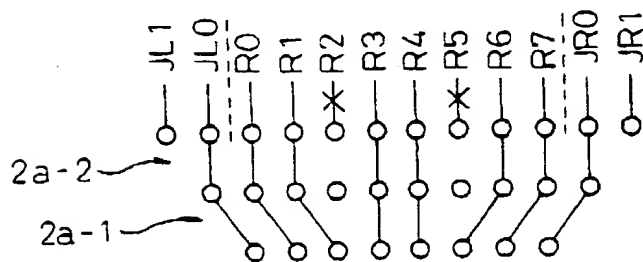
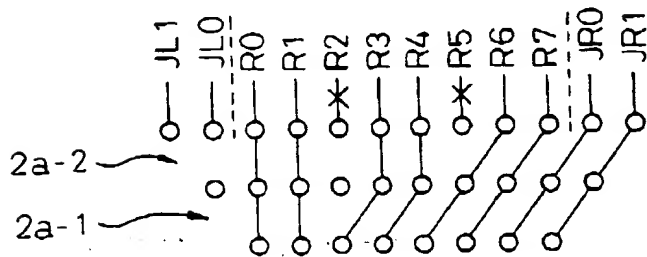


Fig. 101



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Fig. 102

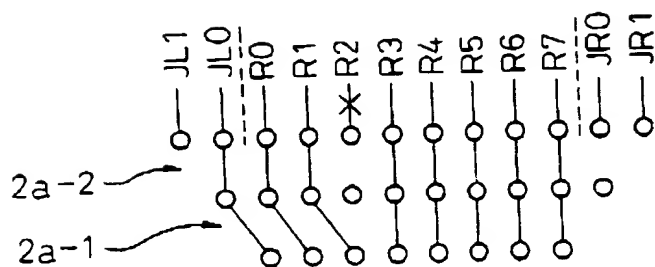


Fig. 103

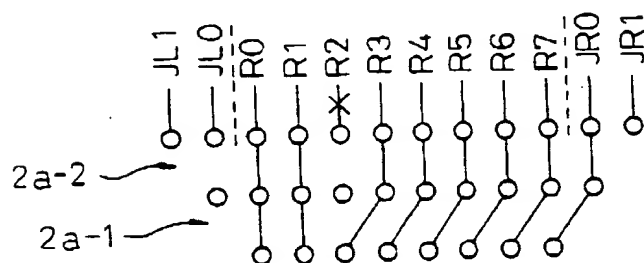
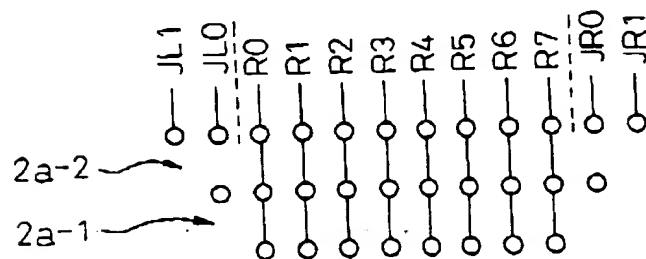


Fig. 104



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Fig. 105

SELECTING LINES	JL1	JL0	R0	R1	R2	R3	R4	R5	R6	R7	JR0	JR1
OUTPUT OF SECOND SHIFT REDUNDANCY CONTROL CIRCUIT UNIT	H	H	H	L	L	L	L	L	L	L	L	L
	L	L	L	L	L	L	L	L	L	L	H	H
	→	→	→	X	↑	X	↑	X	↑	X	→	→
OUTPUT OF FIRST SHIFT REDUNDANCY CONTROL CIRCUIT UNIT	H	H	H	H	H	L	L	L	L	L	L	L
	L	L	L	L	L	L	L	L	H	H	H	H
	→	→	→	→	→	X	↑	X	→	→	→	→
OUTPUT OF SHIFT REDUNDANCY FUSE CIRCUIT UNIT	H	H	H	L	H	L	H	L	H	L	H	H
FUSE	CUT	CUT		CUT		CUT		CUT		CUT	CUT	CUT

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Fig.106

SELECTING LINES	JL1	JL0	R0	R1	R2	R3	R4	R5	R6	R7	JR0	JR1
OUTPUT OF SECOND SHIFT REDUNDANCY CONTROL CIRCUIT UNIT	H L →	H L →	H L →	L L X	L L ↑	L L X	L L ↑	L L ↑	L L X	L L ↑	L L ↑	L L ↑
OUTPUT OF FIRST SHIFT REDUNDANCY CONTROL CIRCUIT UNIT	H L →	H L →	H L →	H L →	H L →	L L X	L L ↑	L L ↑	L L X	L L →	L H →	L H →
OUTPUT OF SHIFT REDUNDANCY FUSE CIRCUIT UNIT FUSE	H CUT	H CUT	H CUT	L CUT	H CUT	L CUT	H CUT	H CUT	L CUT	H CUT	H CUT	L CUT

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Fig.107

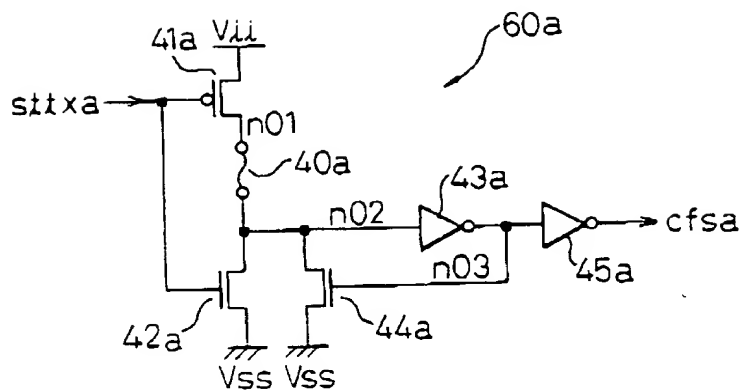
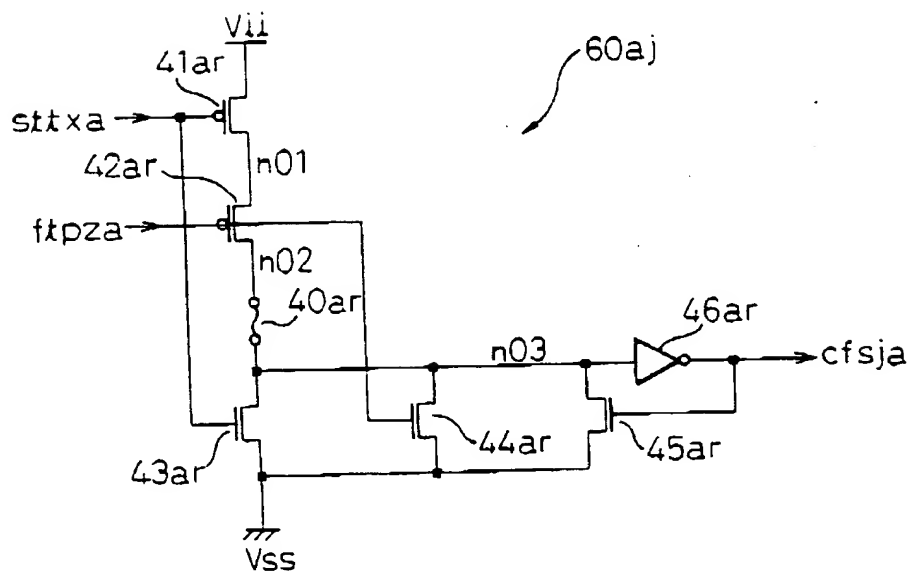


Fig.108



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Fig.109

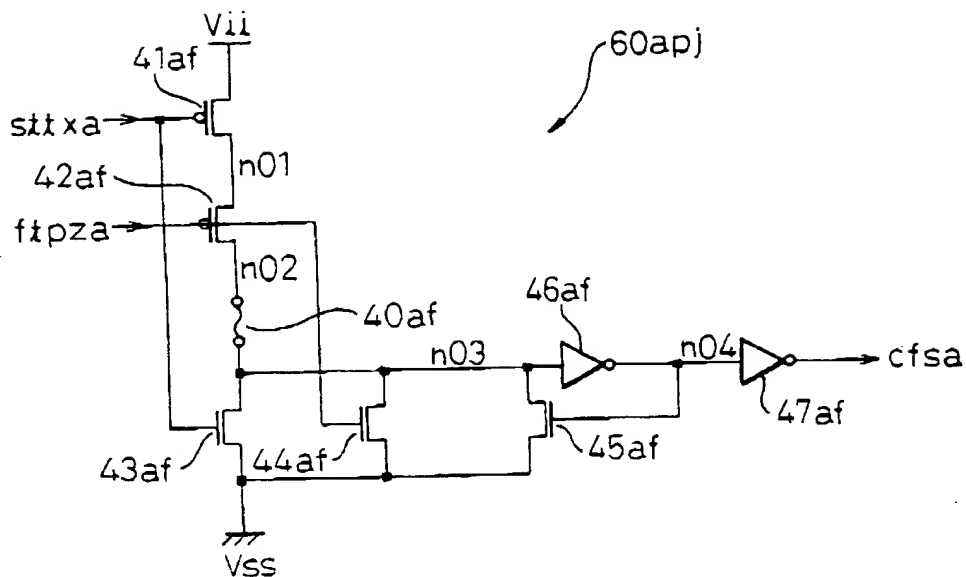
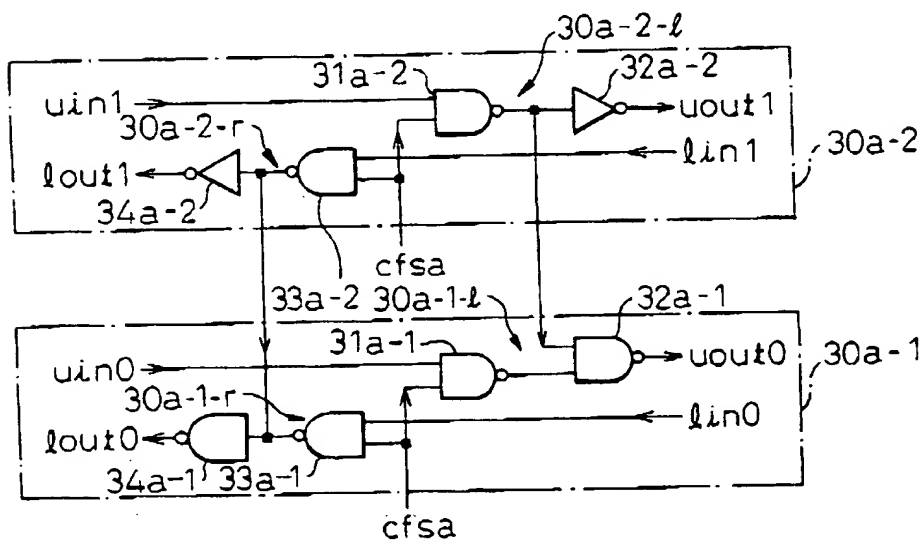


Fig.110



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Fig.111

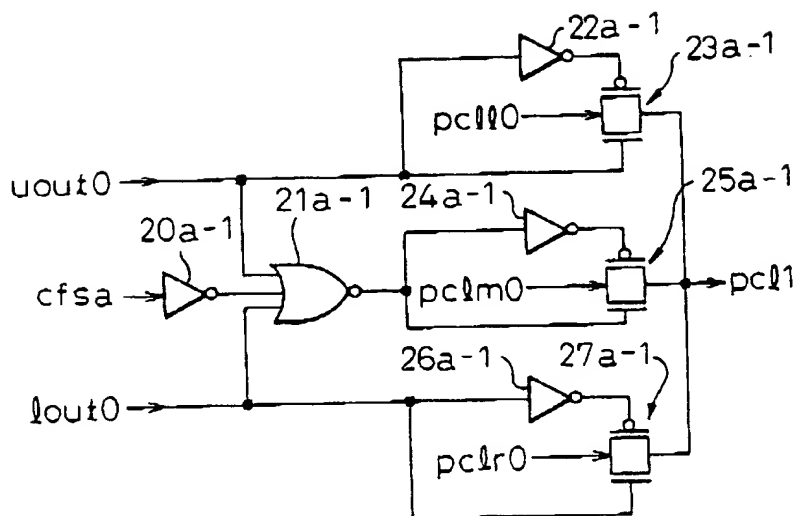
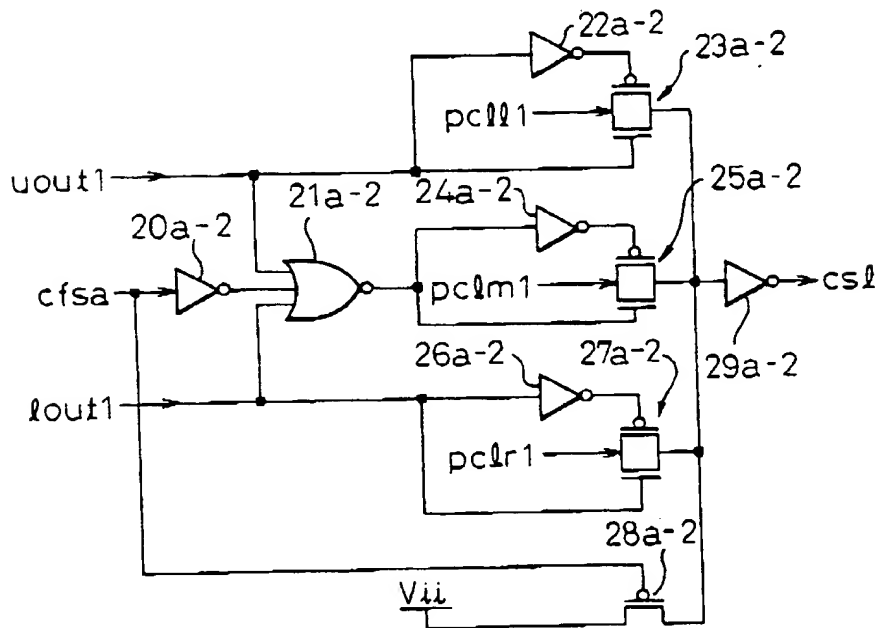


Fig.112



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Fig. 113

